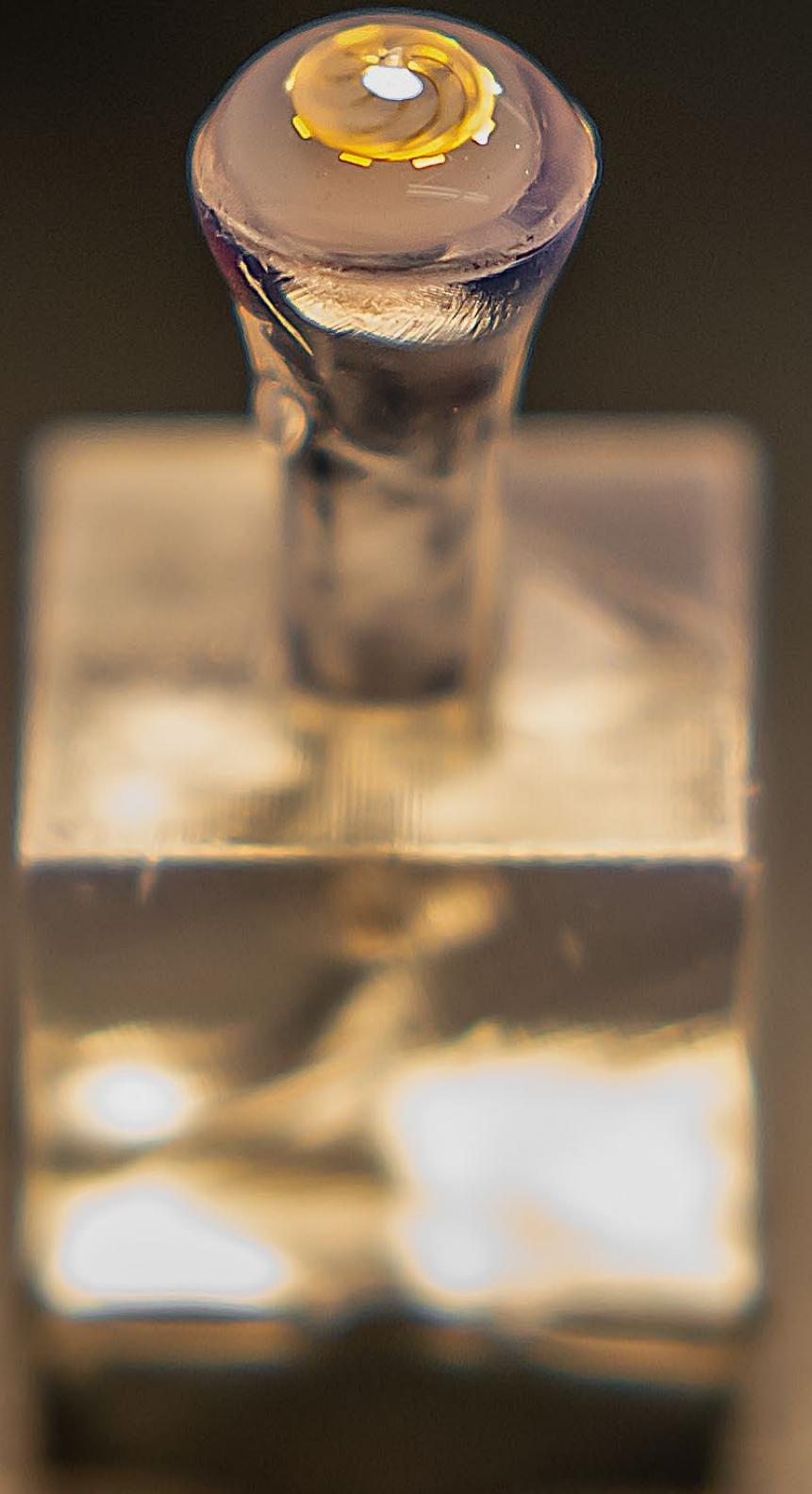


# Modular High-throughput Computing Pipelines for Scalable **Neuronal Electrophysiology** Research

*Engineering In Vitro computing platforms with Frontera*



Seung Hyun Kim  
Ivan Raikov  
Frithjof Gressman

Pls:  
Mattia Gazzola, UIUC  
Ivan Soltesz, Stanford  
Lawrence Rauchwerger, UIUC



Project Overview — Mind In Vitro



Biophysical Neural Network Simulator



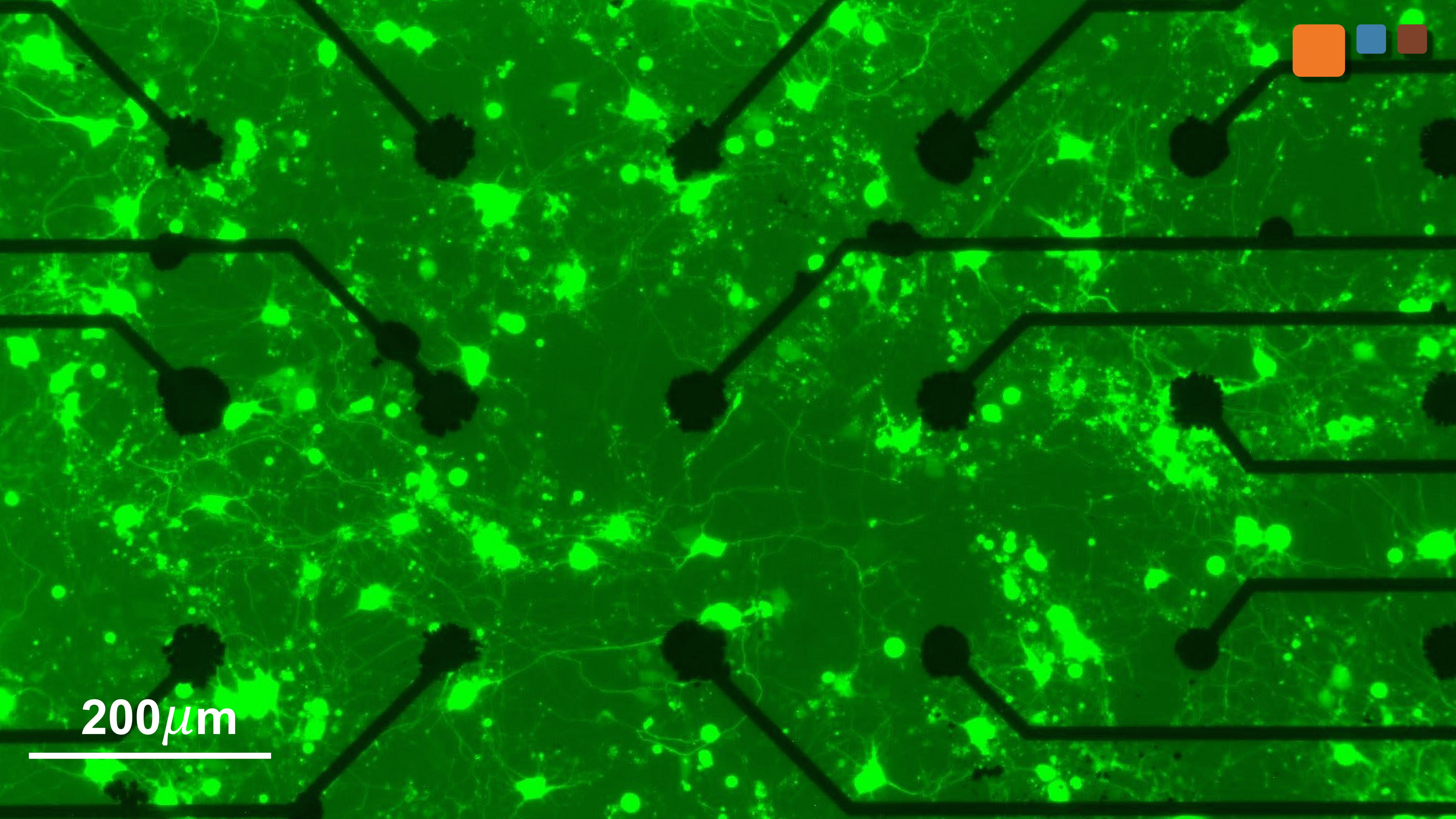
Modular and Scalable Analysis Pipeline

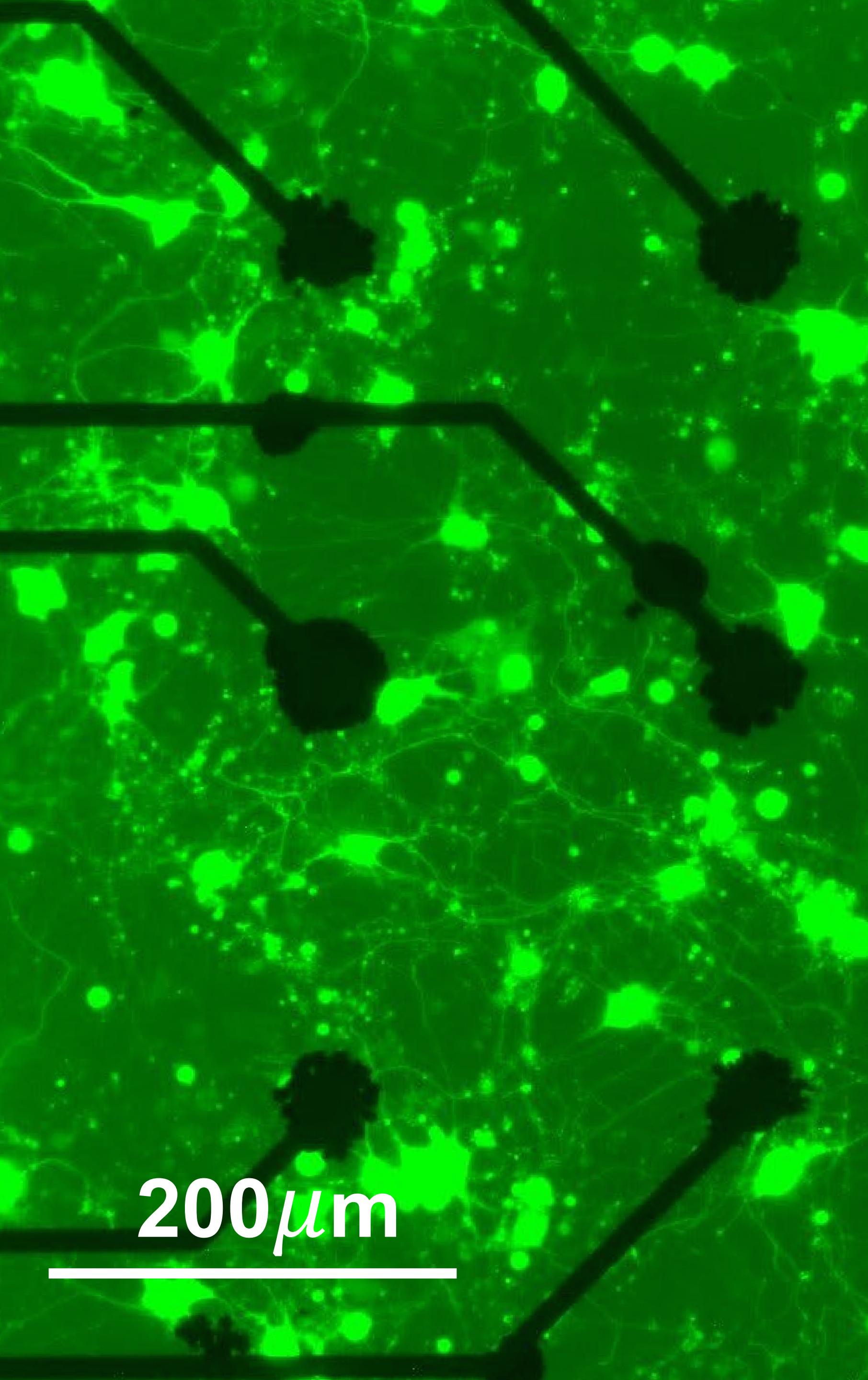


# Computing with cellular substrates



200 $\mu$ m





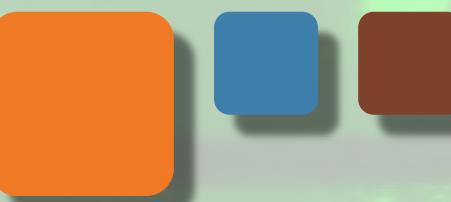
200 $\mu$ m

## Neurons:

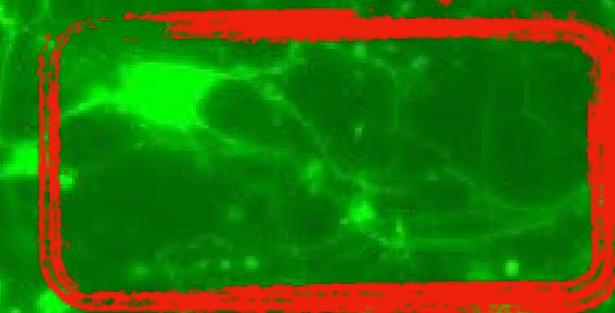
- Embryonic Stem cells derived
  - Motoneuron
  - Cortical neuron
- ChannelRhodopsin (ChR2)

## Recording Interface:

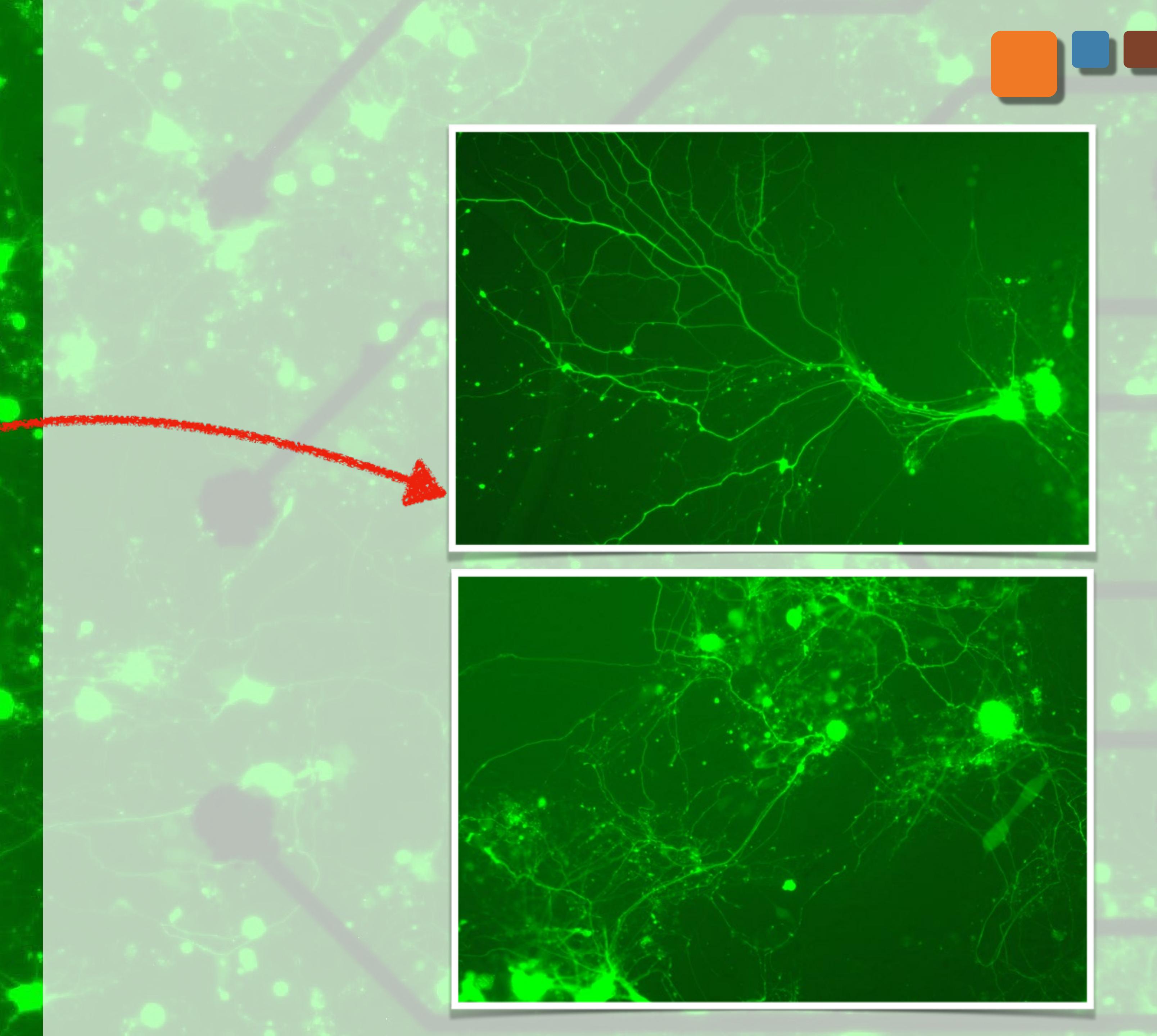
- Multi-electrode array: 512 Electrodes (30  $\mu$ m)
- Sampling Rate: 30 kHz

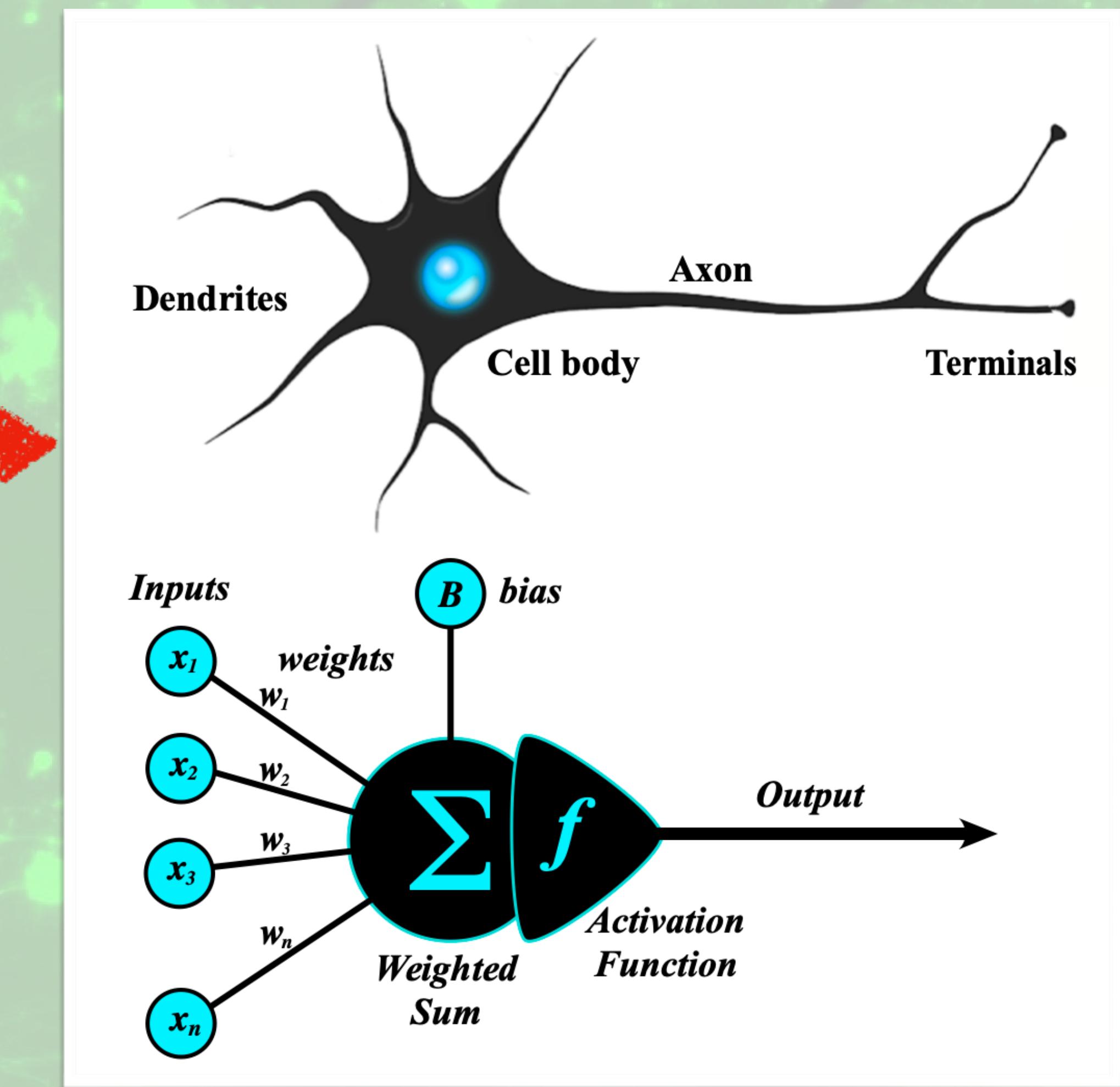
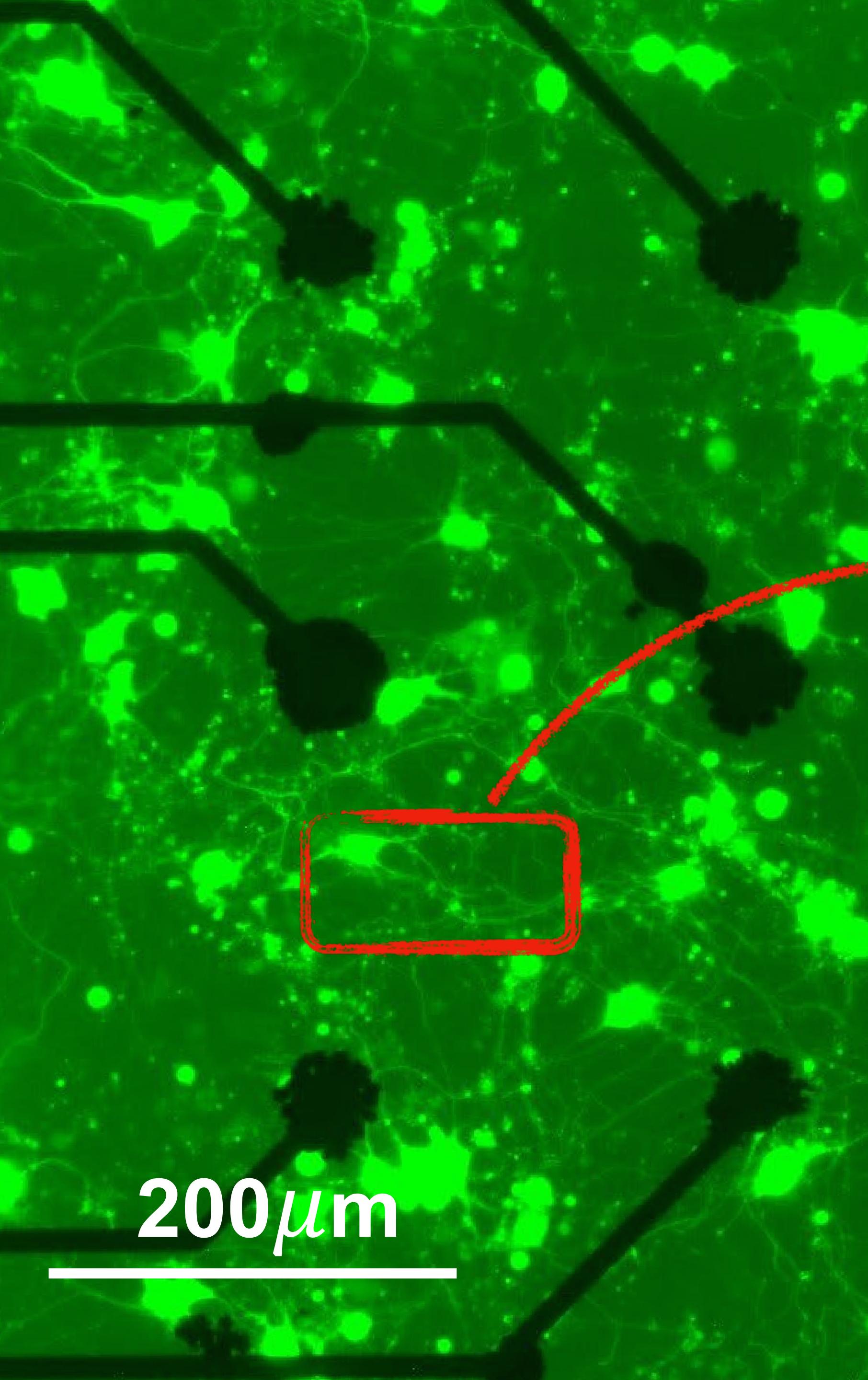
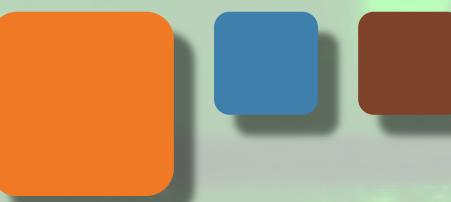


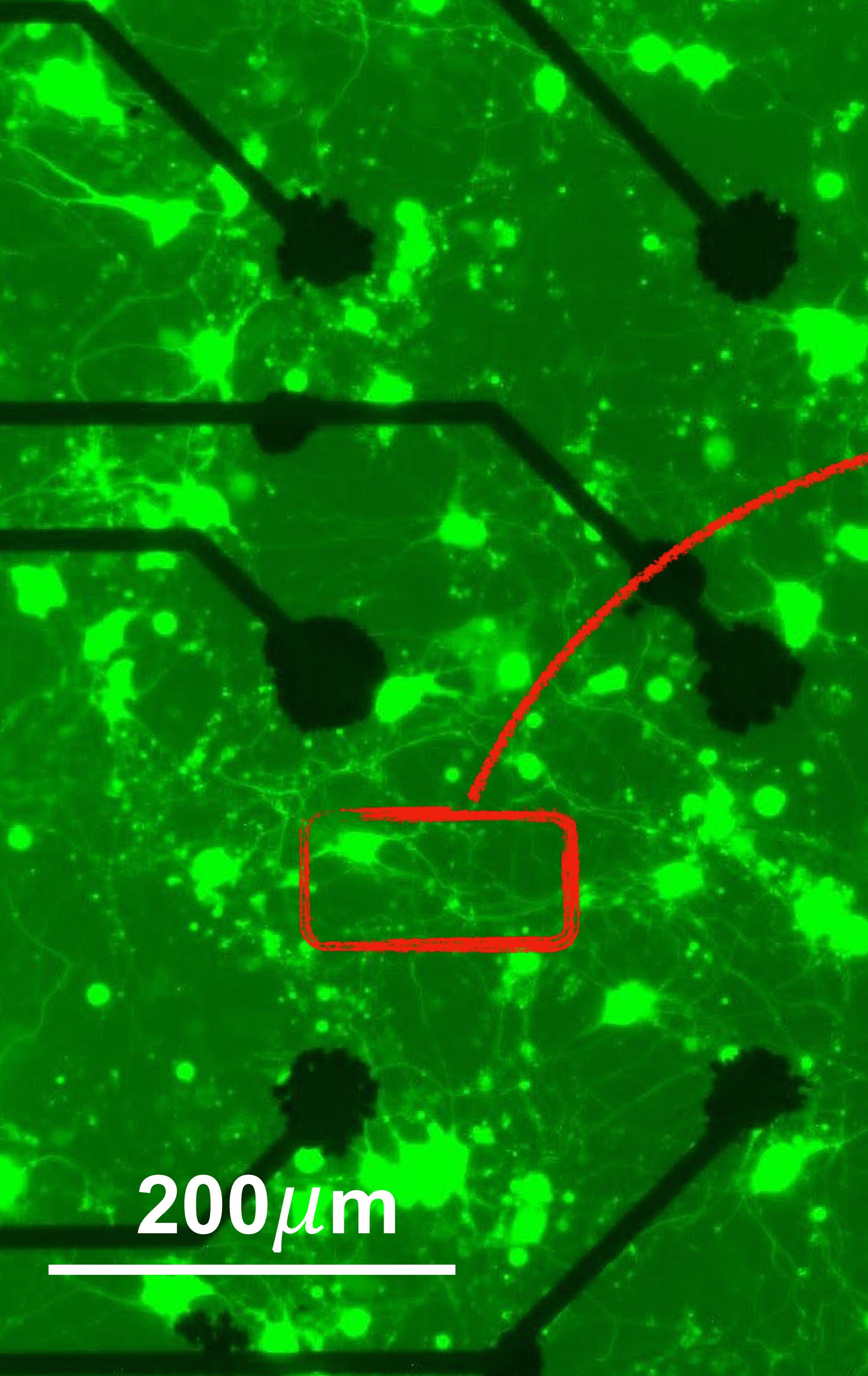
- 80~100k neurons
- X100 synapses per neuron



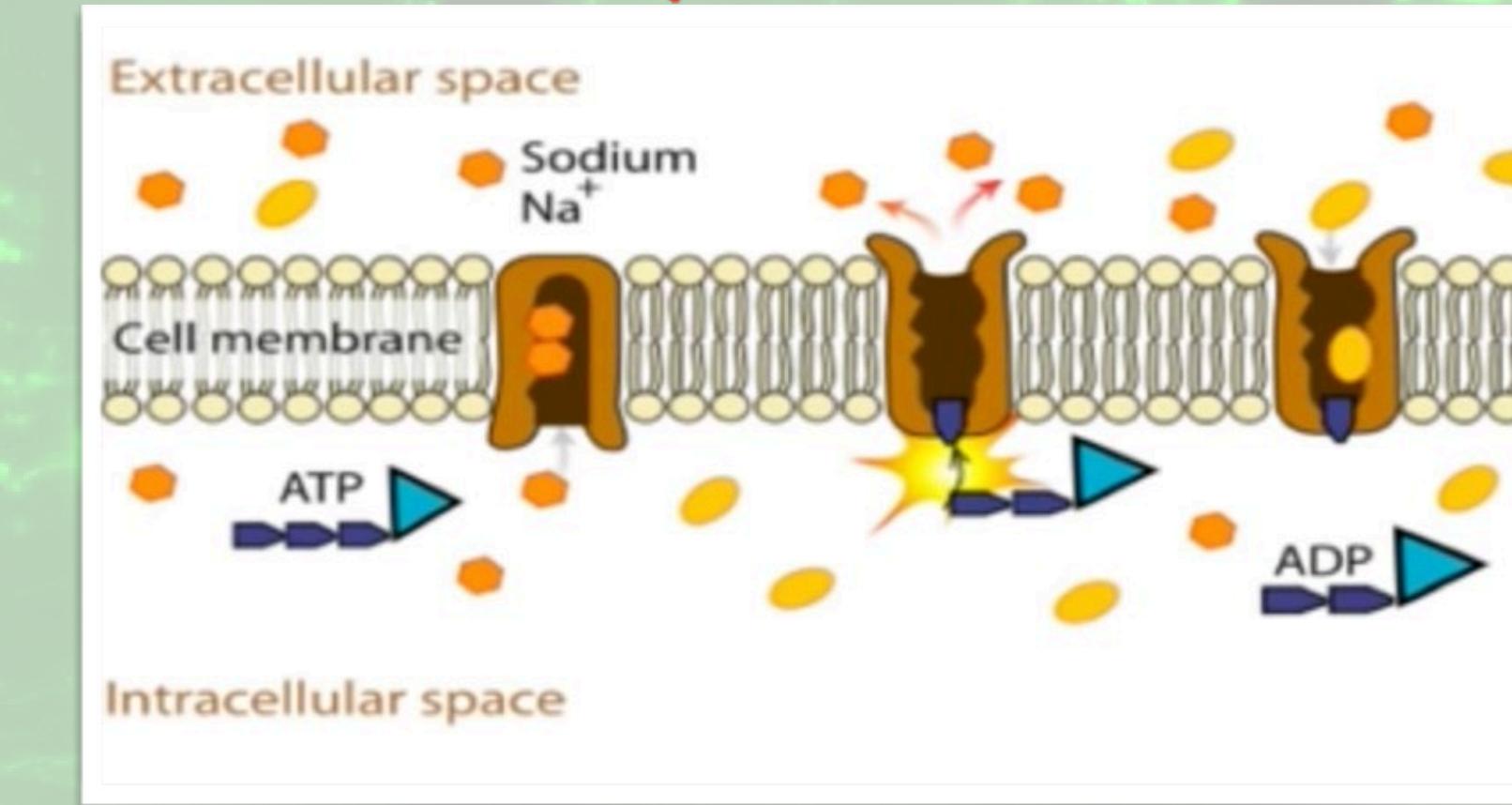
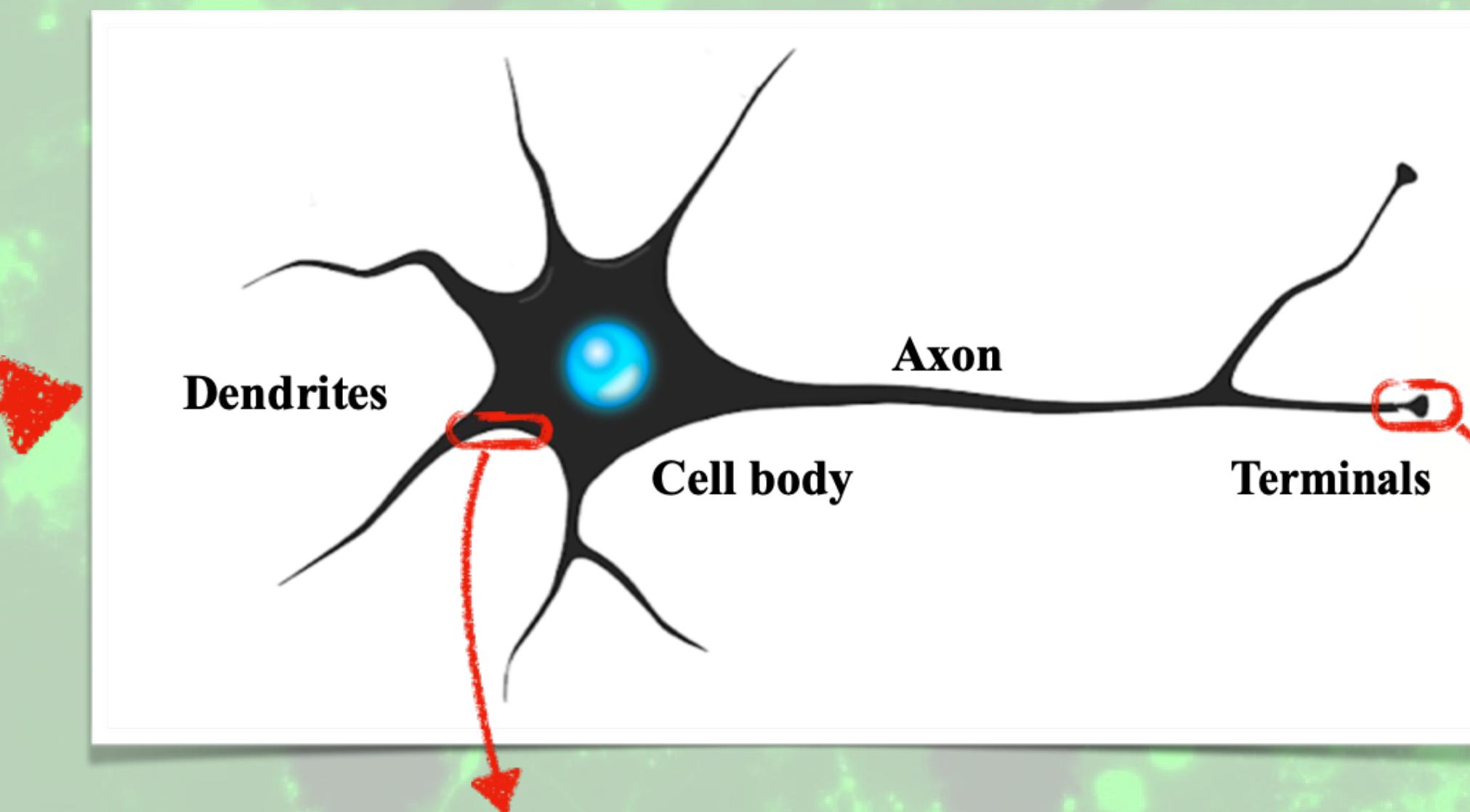
$200\mu\text{m}$



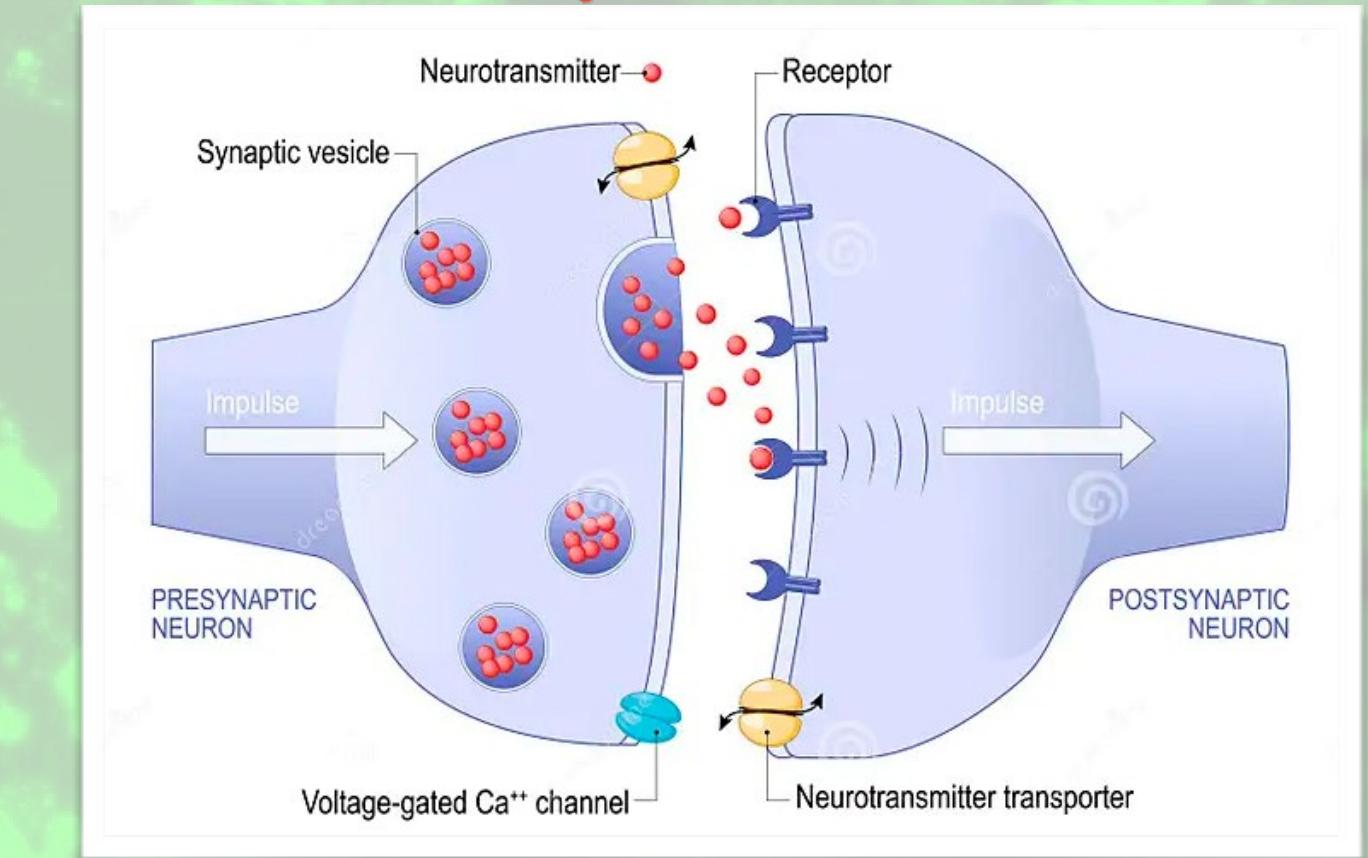




200  $\mu$ m



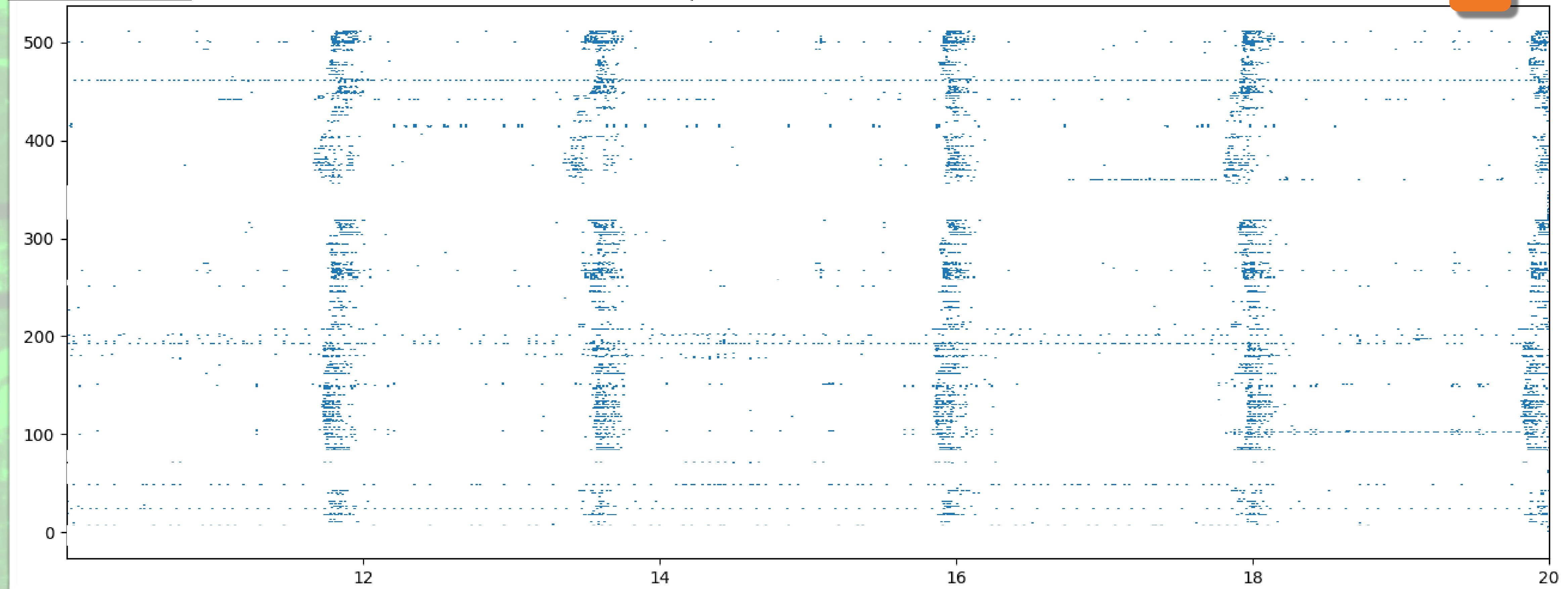
Cell Membrane



Synapses

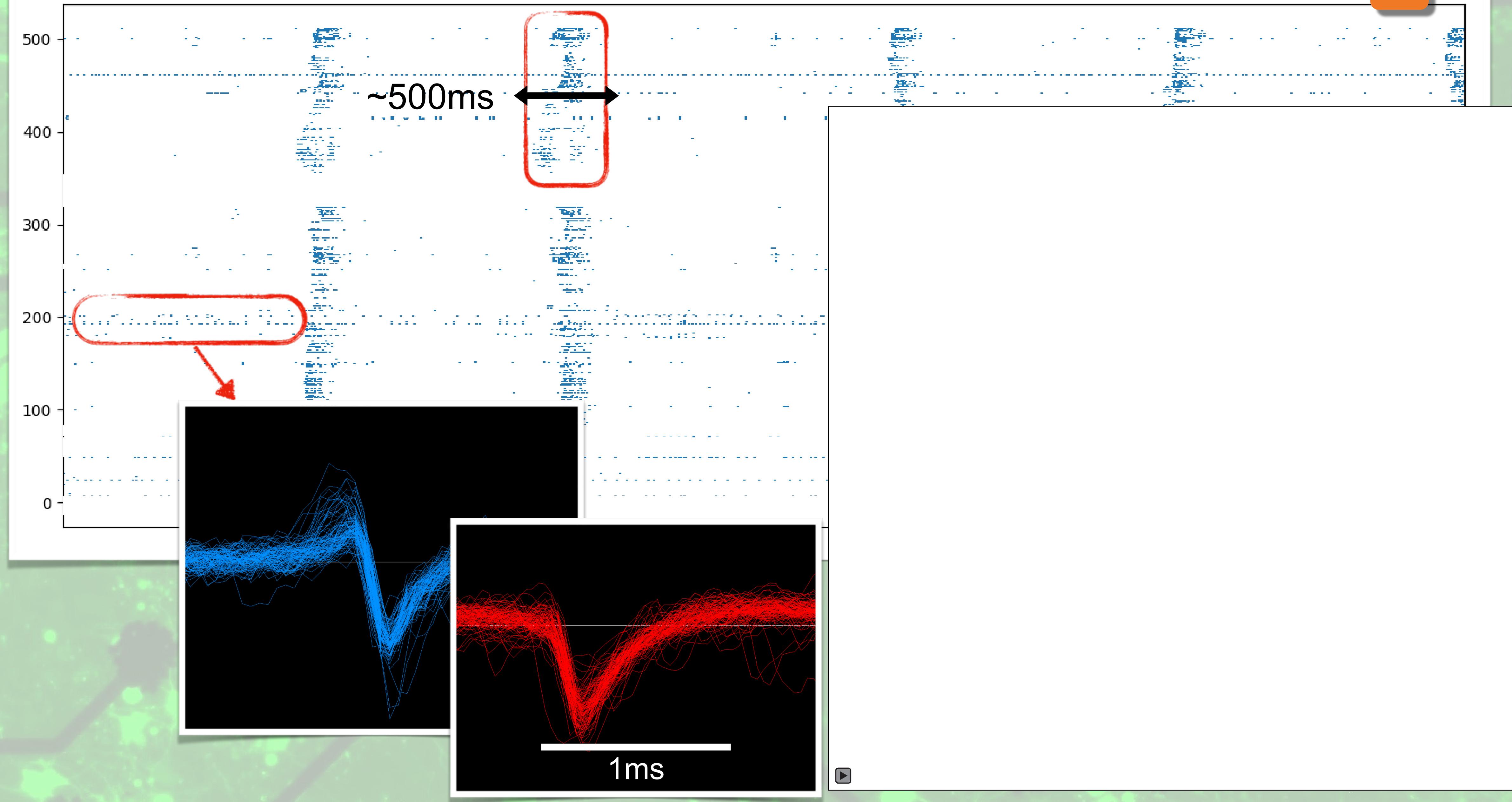
Spontaneous

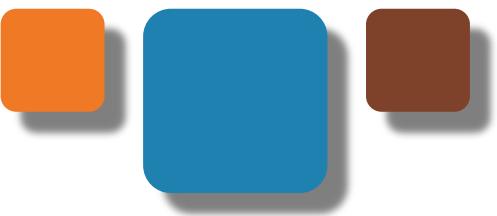
Raster plot (from 10.0002 to 20.0002)



Spontaneous

Raster plot (from 10.0002 to 20.0002)

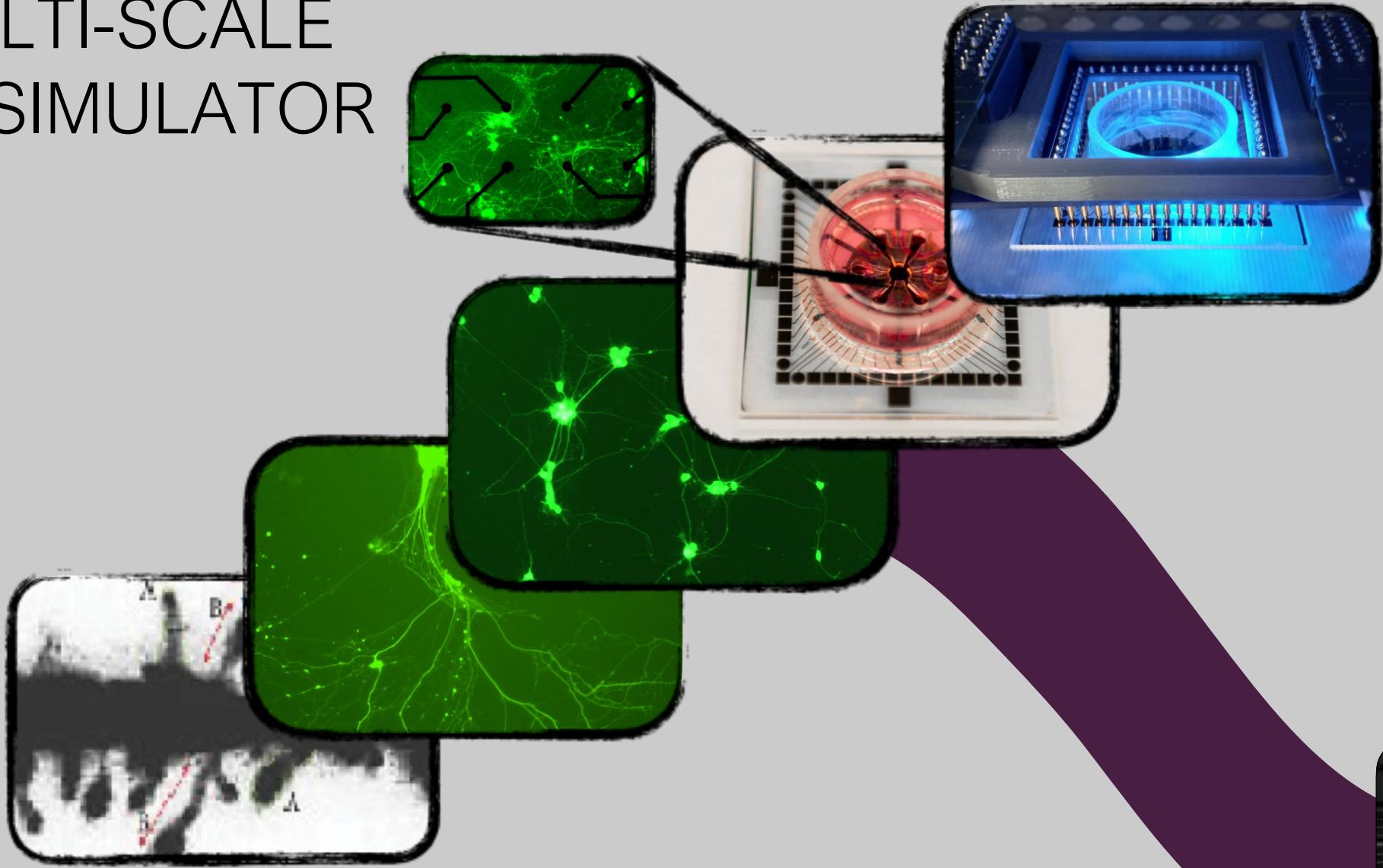




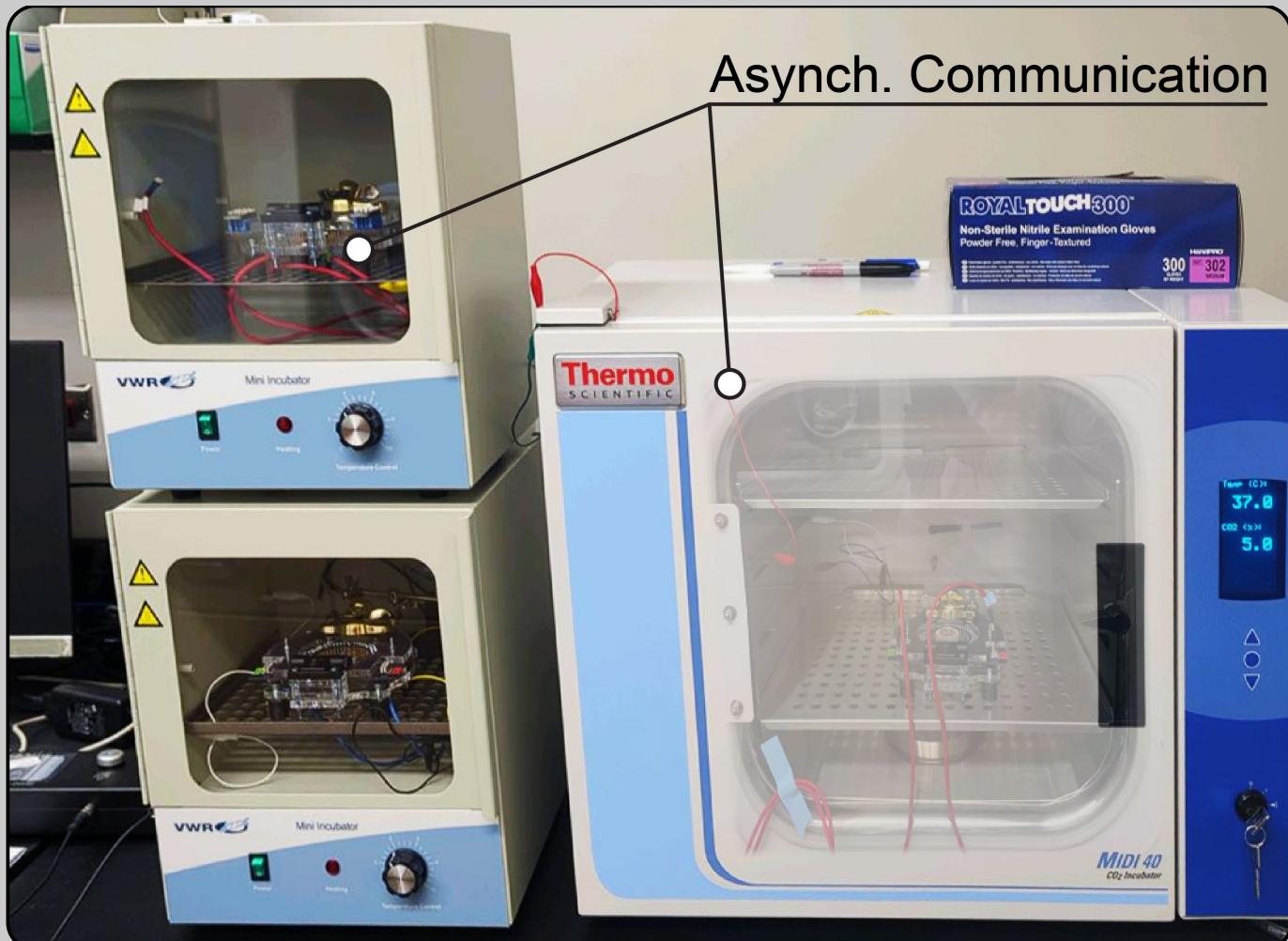
# Computational Problem

Need for a systematic framework to engineer these systems

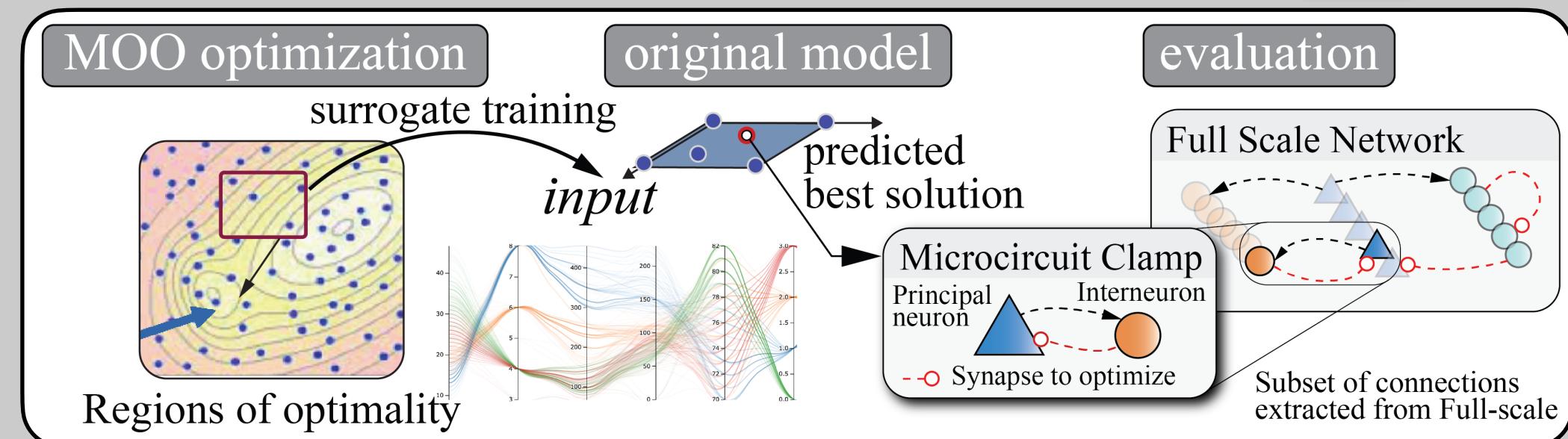
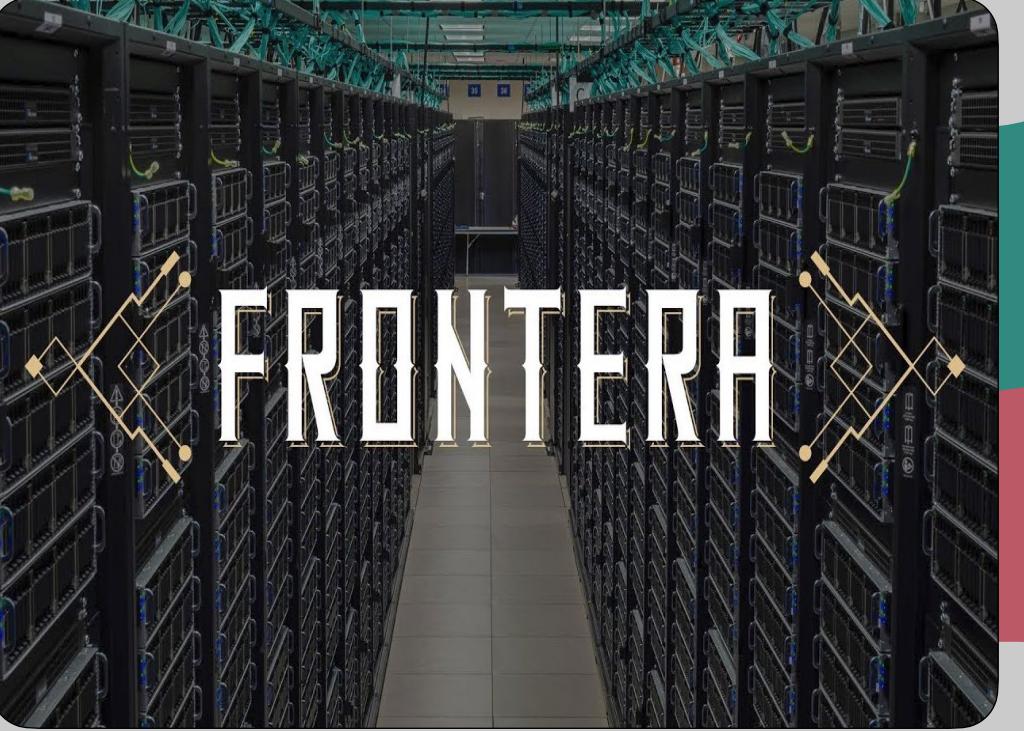
# MULTI-SCALE MiV SIMULATOR



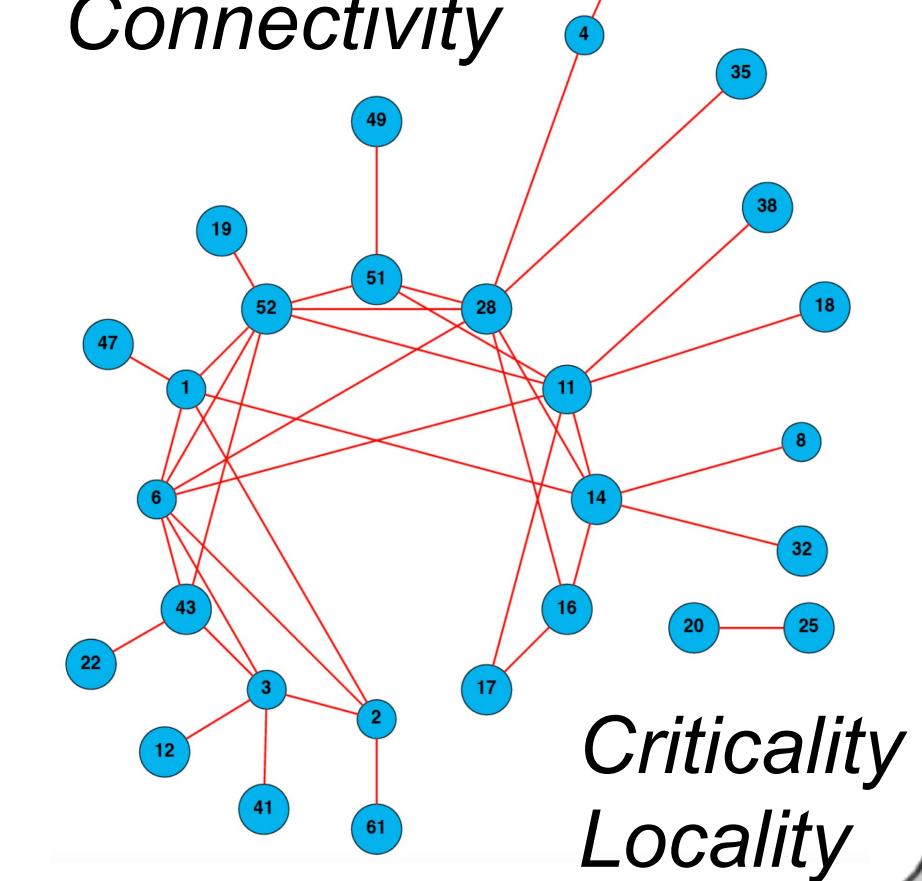
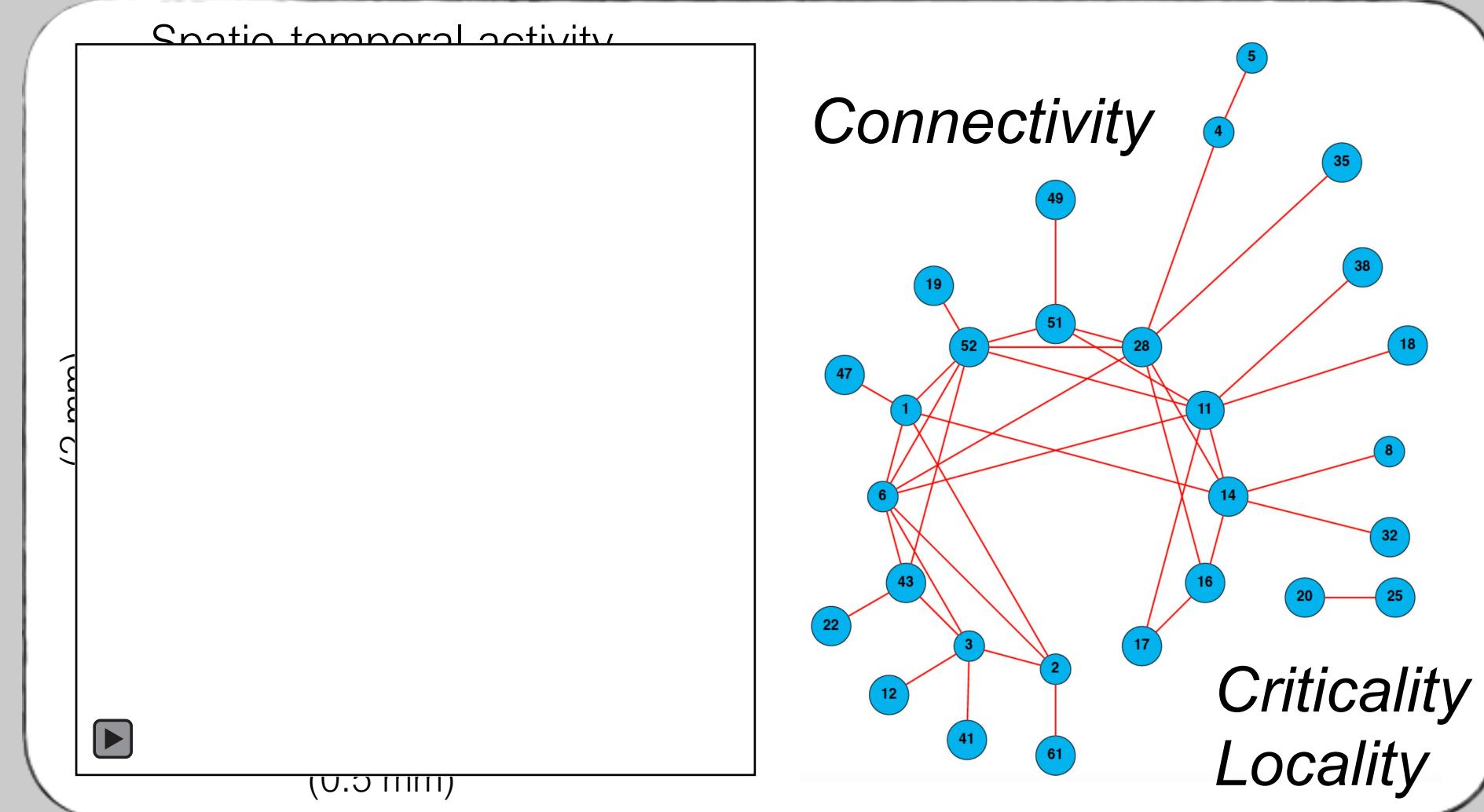
## IN VITRO EXPERIMENT

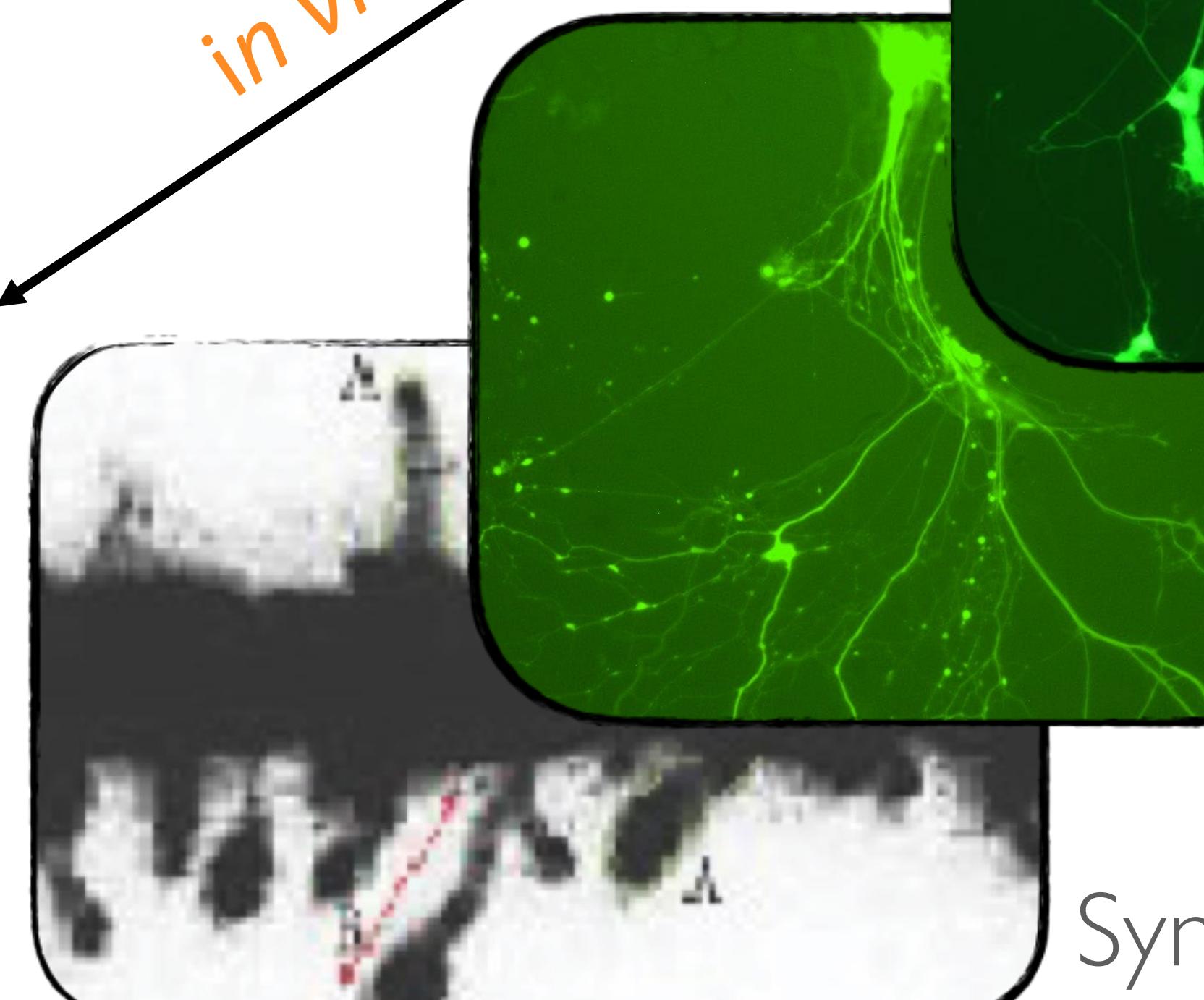


# HPC Framework



## SURROGATE OPTIMIZATION

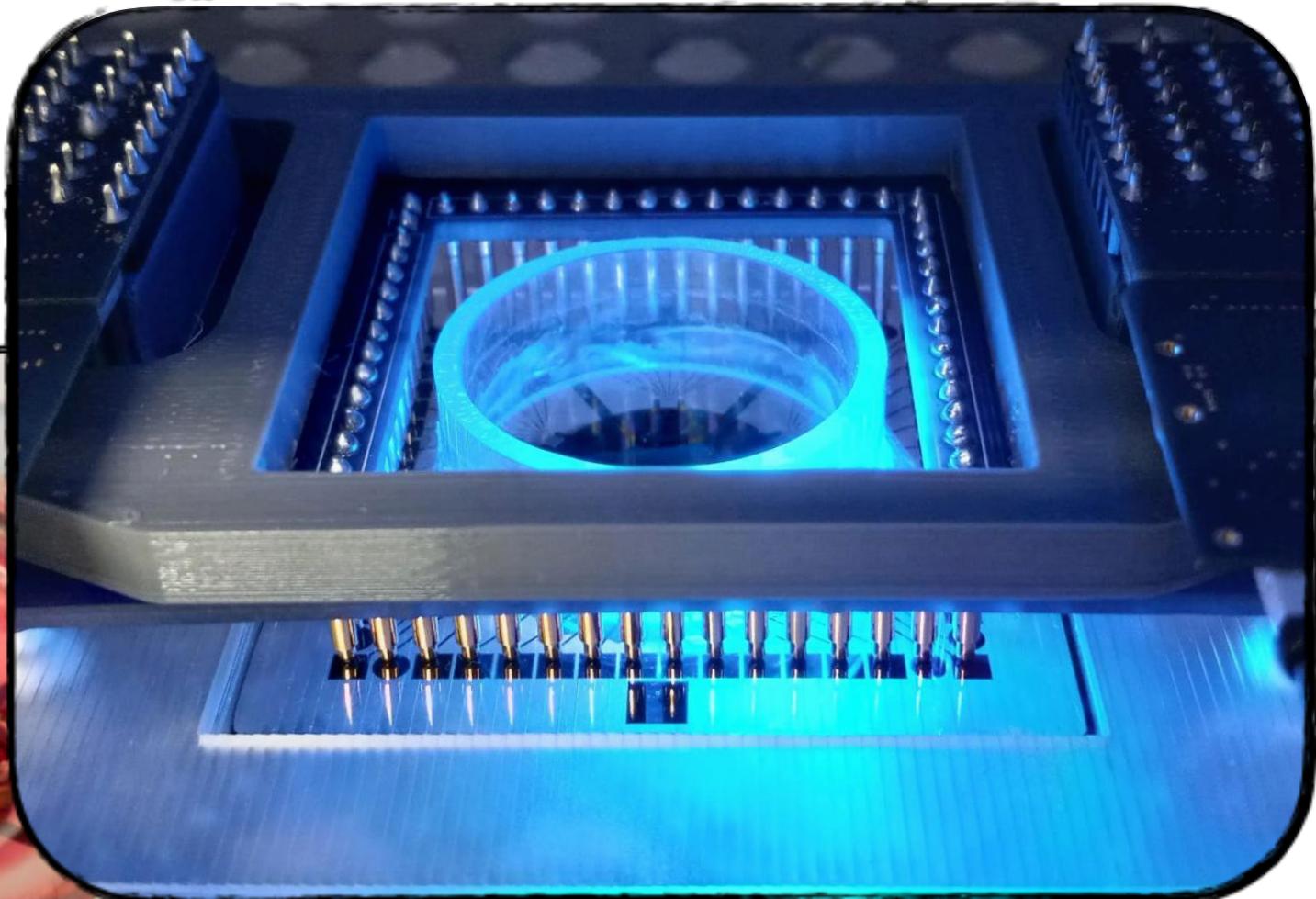
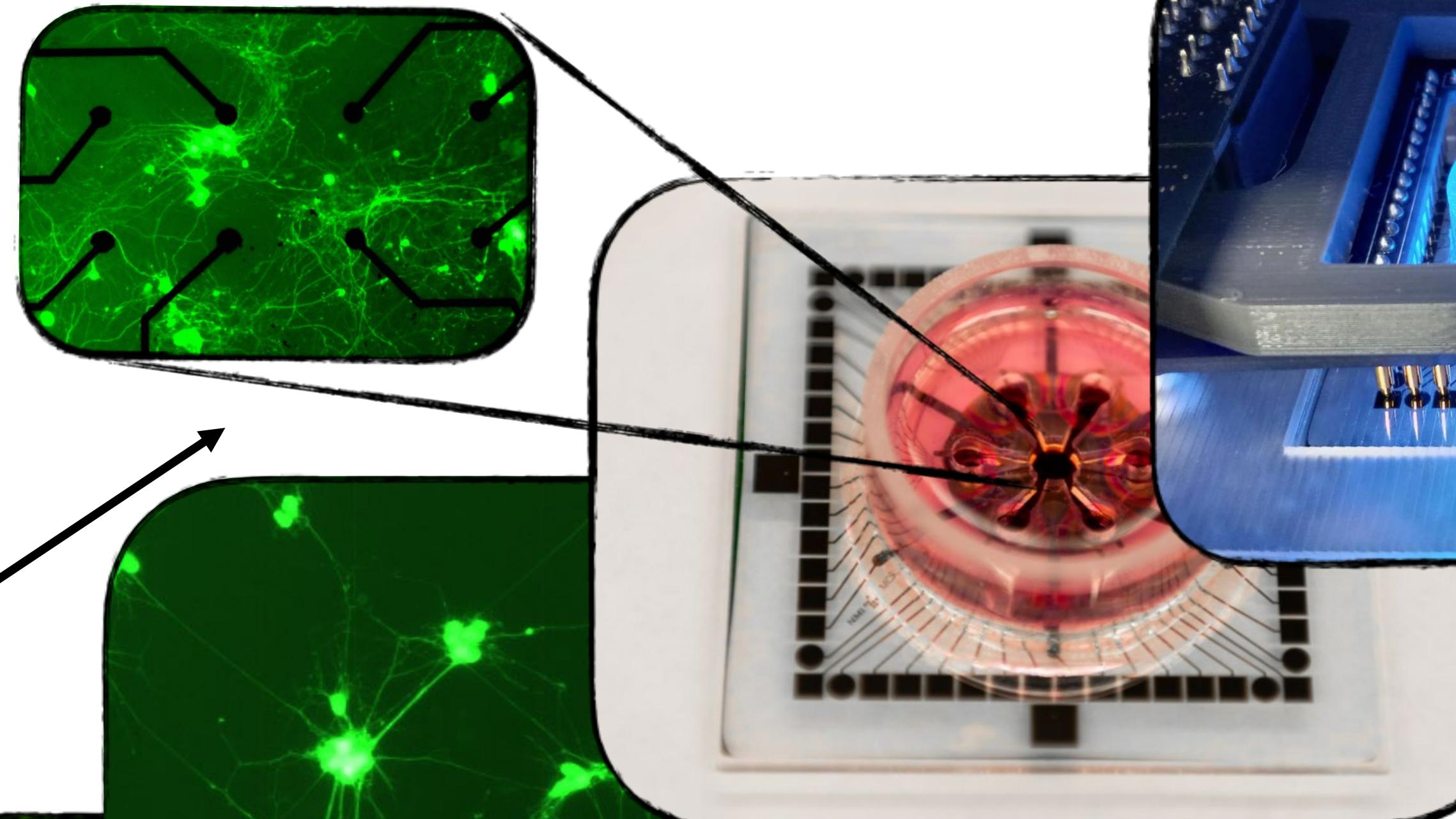




Developed for  
in vivo studies



Synaptic Activity



In Vitro  
experiment

Modular  
HPC Framework

# Biophysical HPC Simulator



HPC Framework  
optimized for Frontera!

single cell

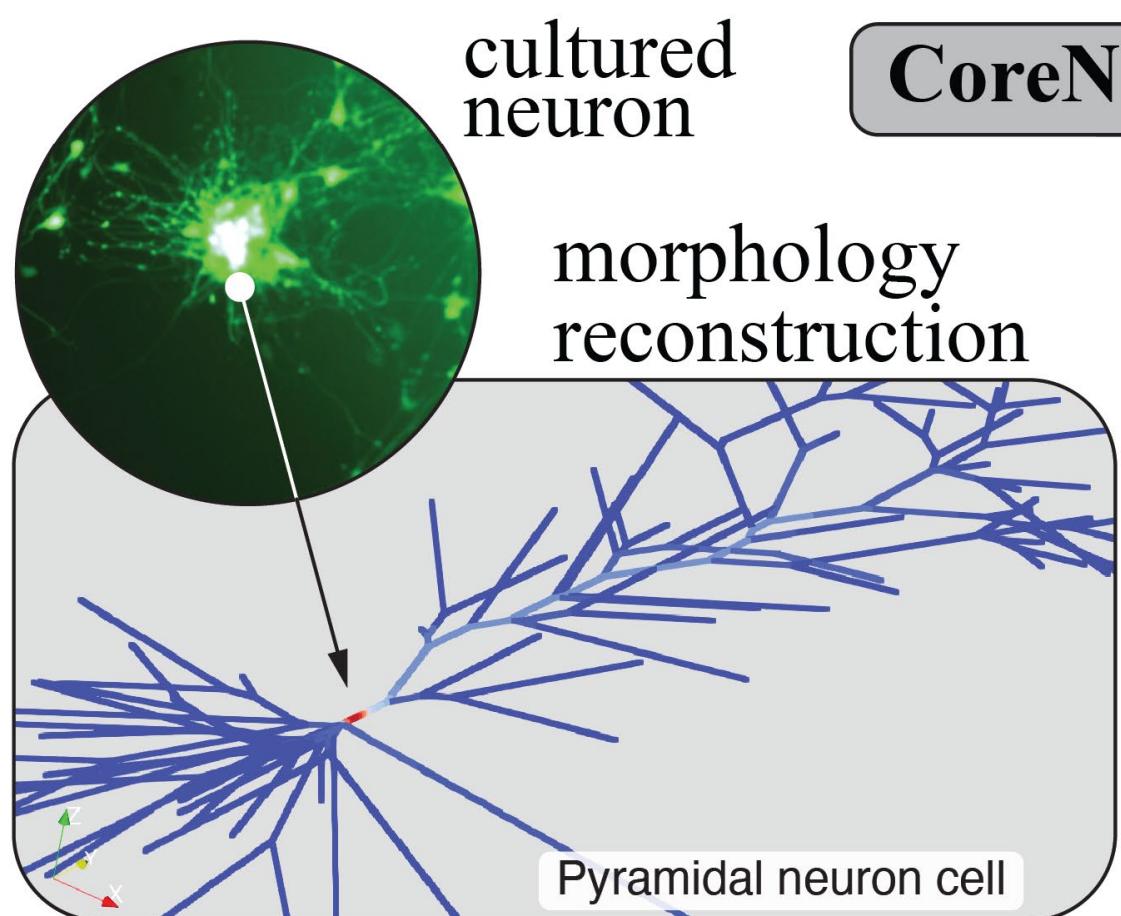
network clamp

microcircuit

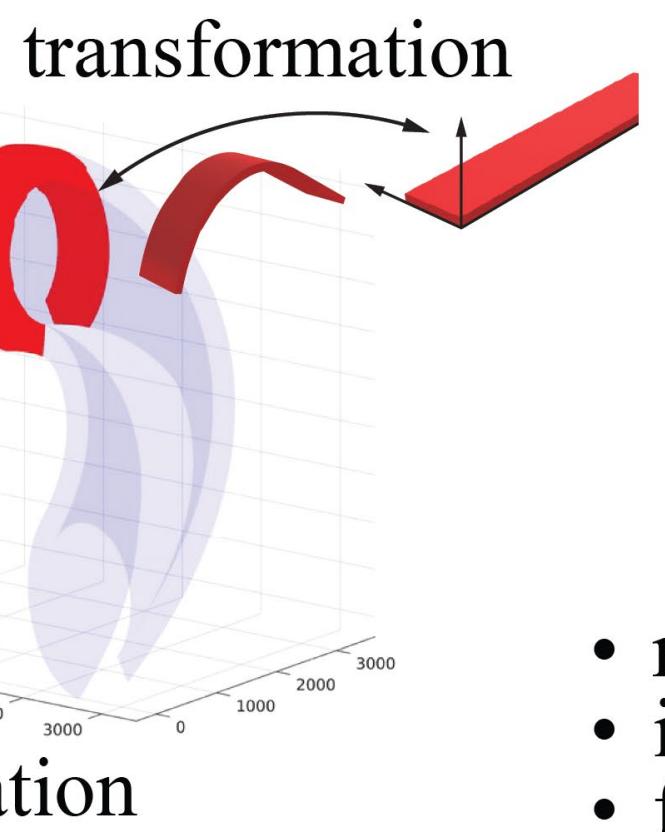
full network

environment

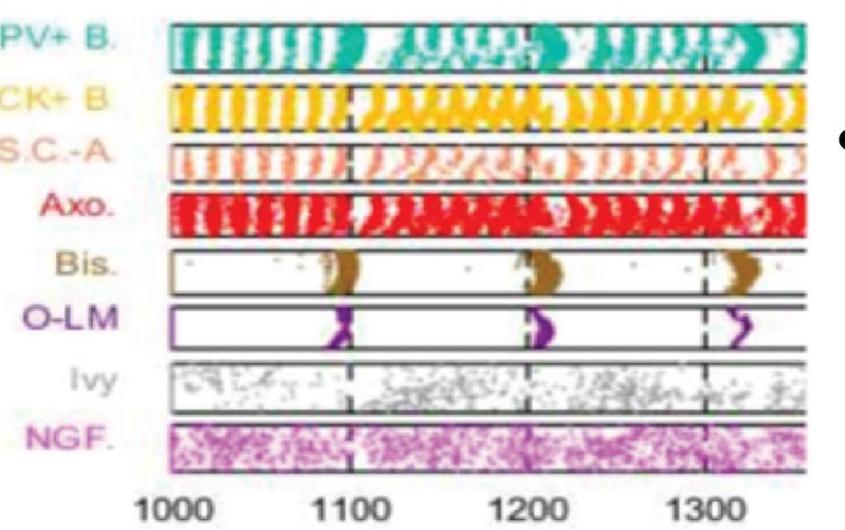
experiment



**CoreNEURON**

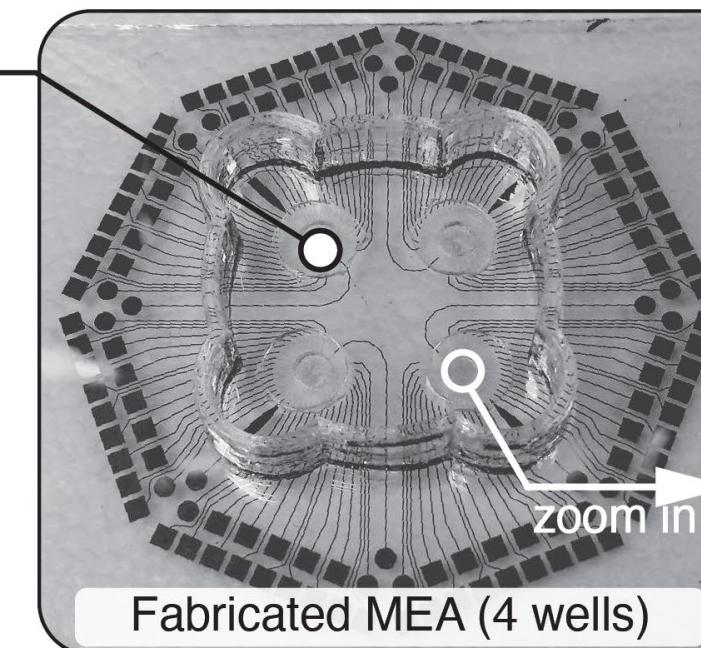


**MiV-Simulator**

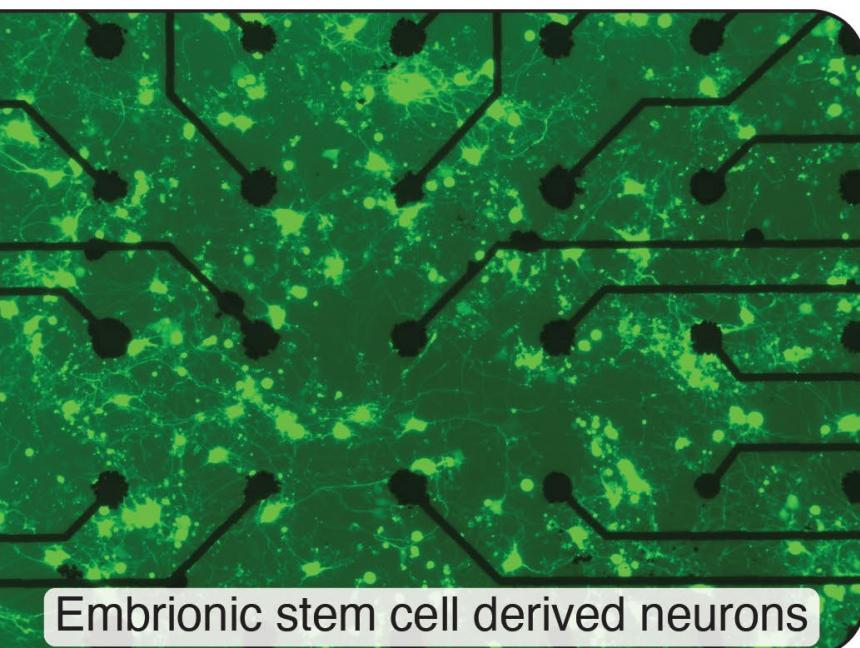


- resting membrane potential
- intrinsic conductances
- firing intensity and rate

• LFP



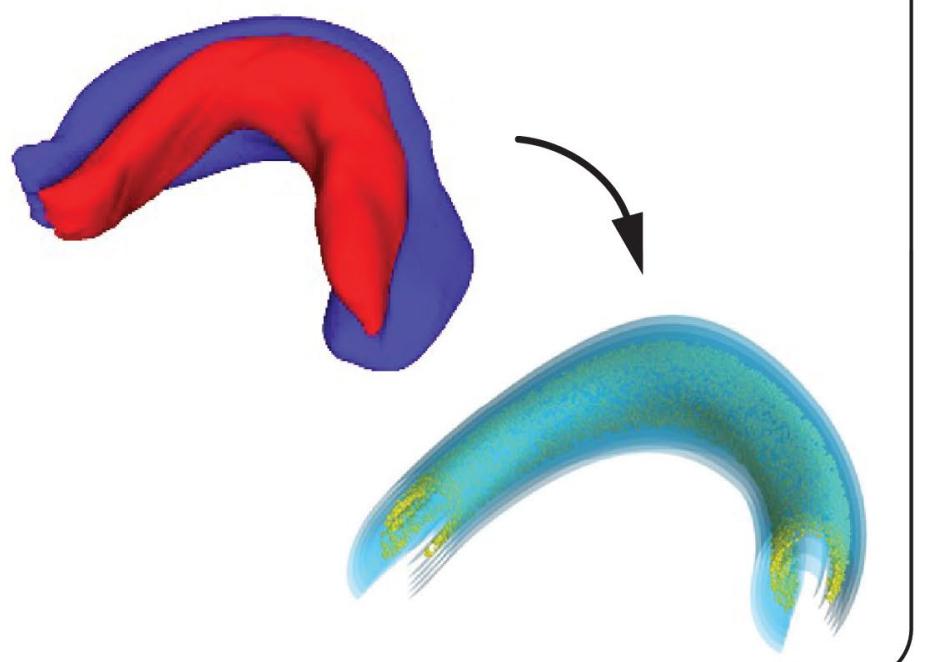
Fabricated MEA (4 wells)



Embrionic stem cell derived neurons

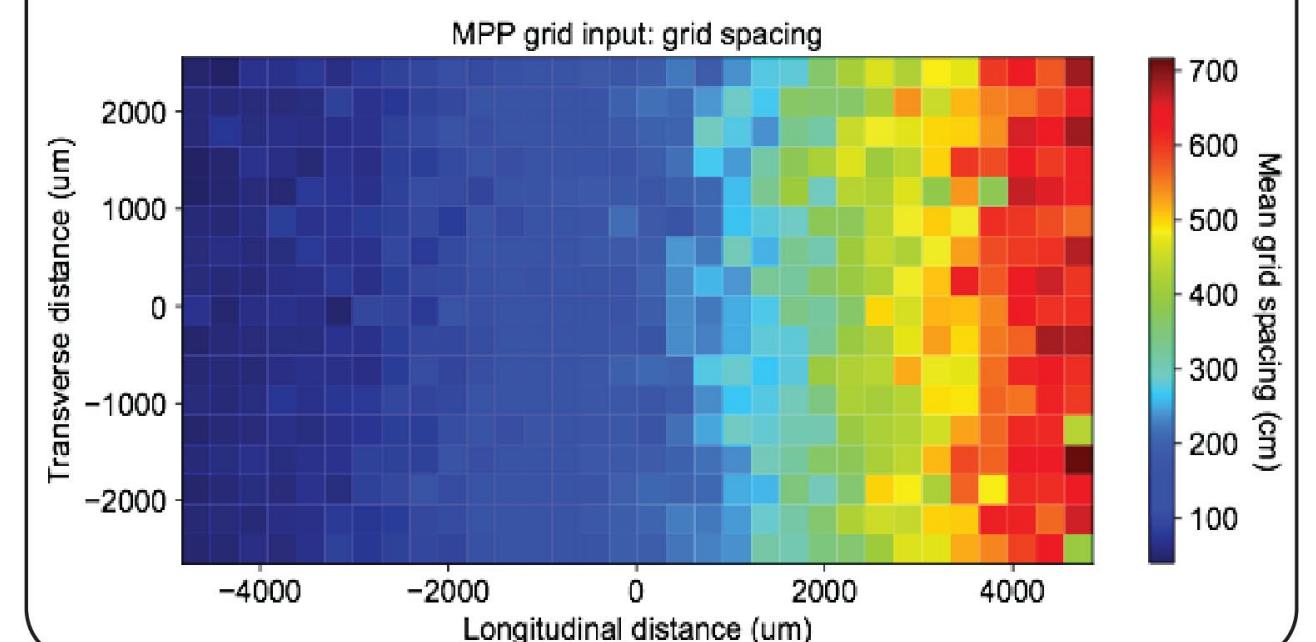
Realistic DG  
3D anatomy

(b)

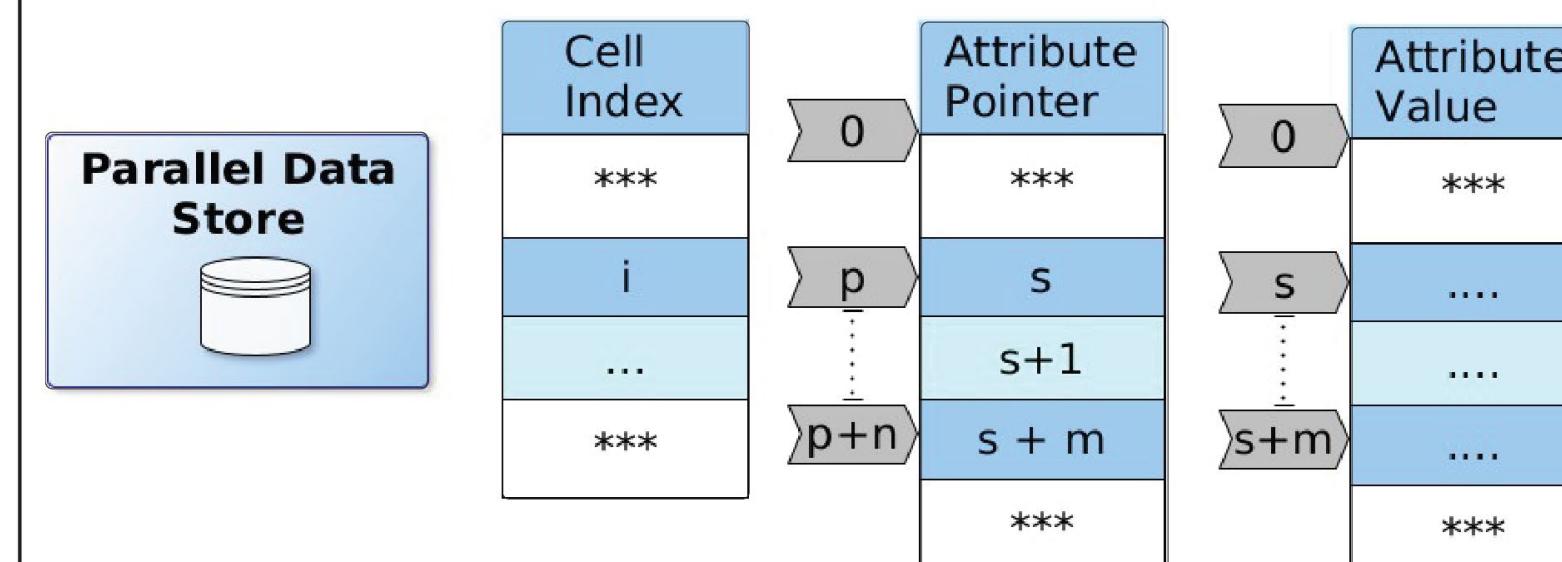


Topographical gradients  
Functional connectivity

(c)



Distributed data-structure for  
biological neuron simulation



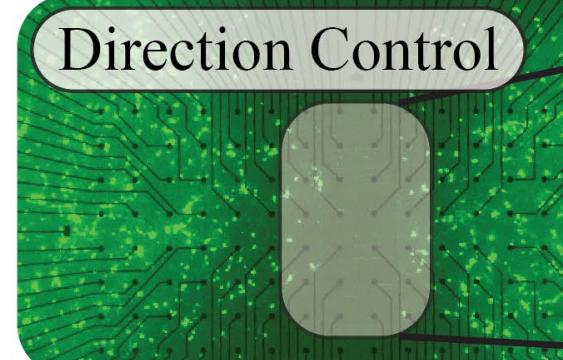
(d)

Excitatory

(e)



Inhibitory

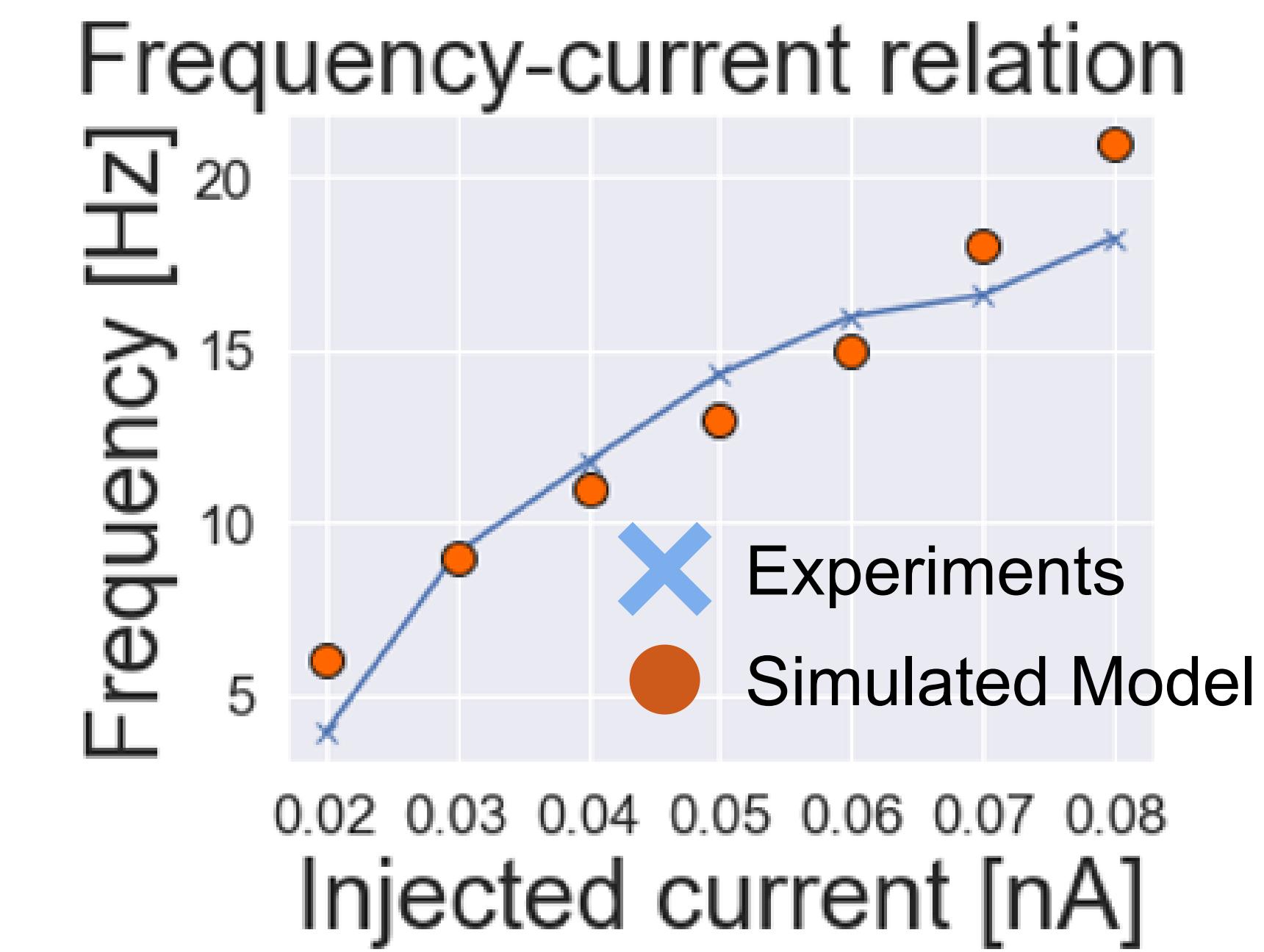
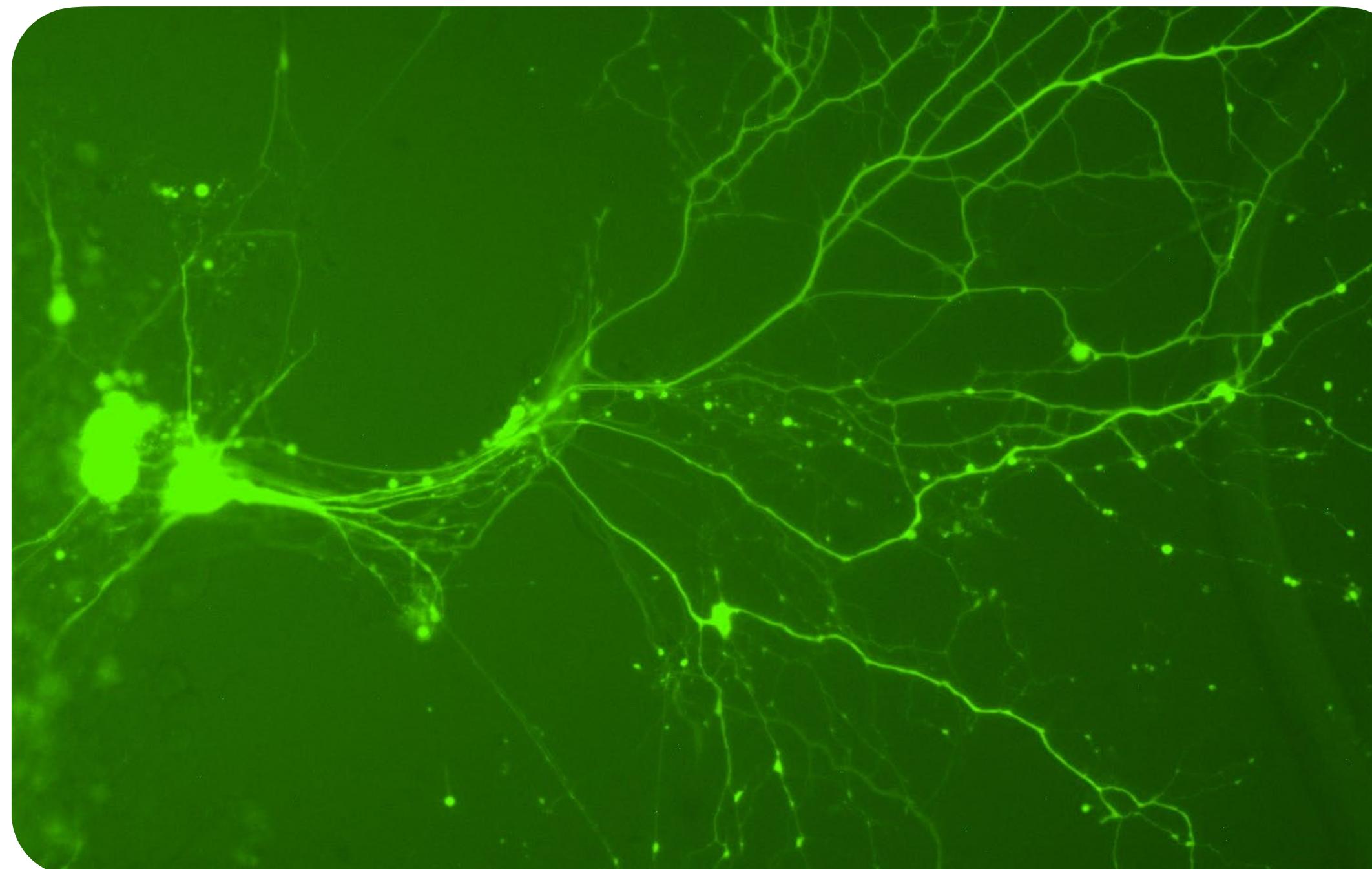
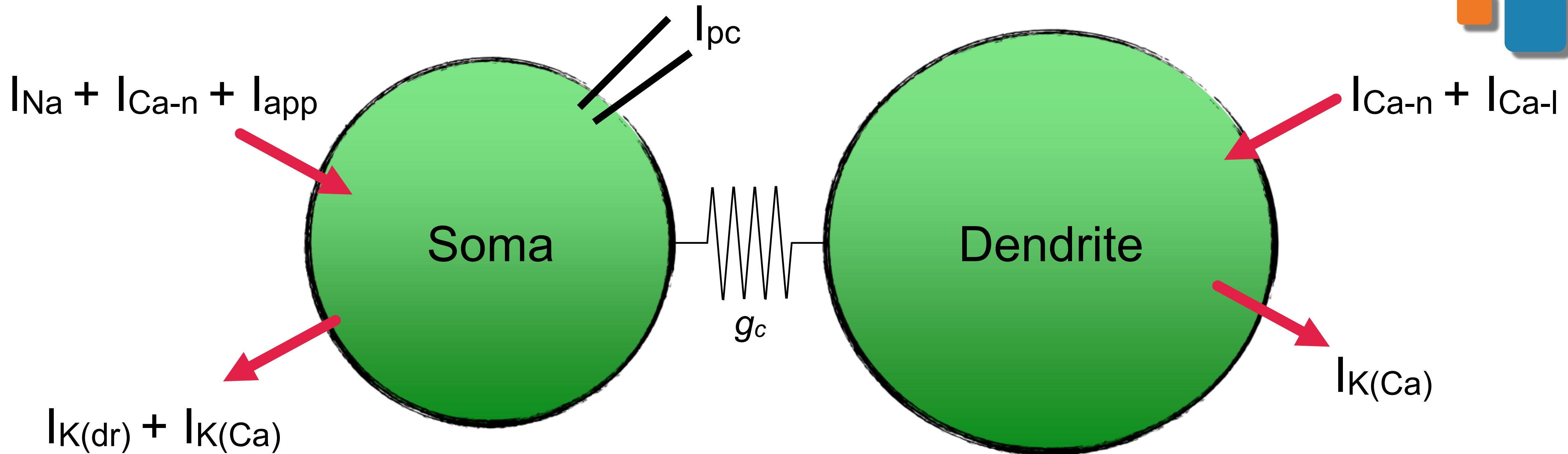


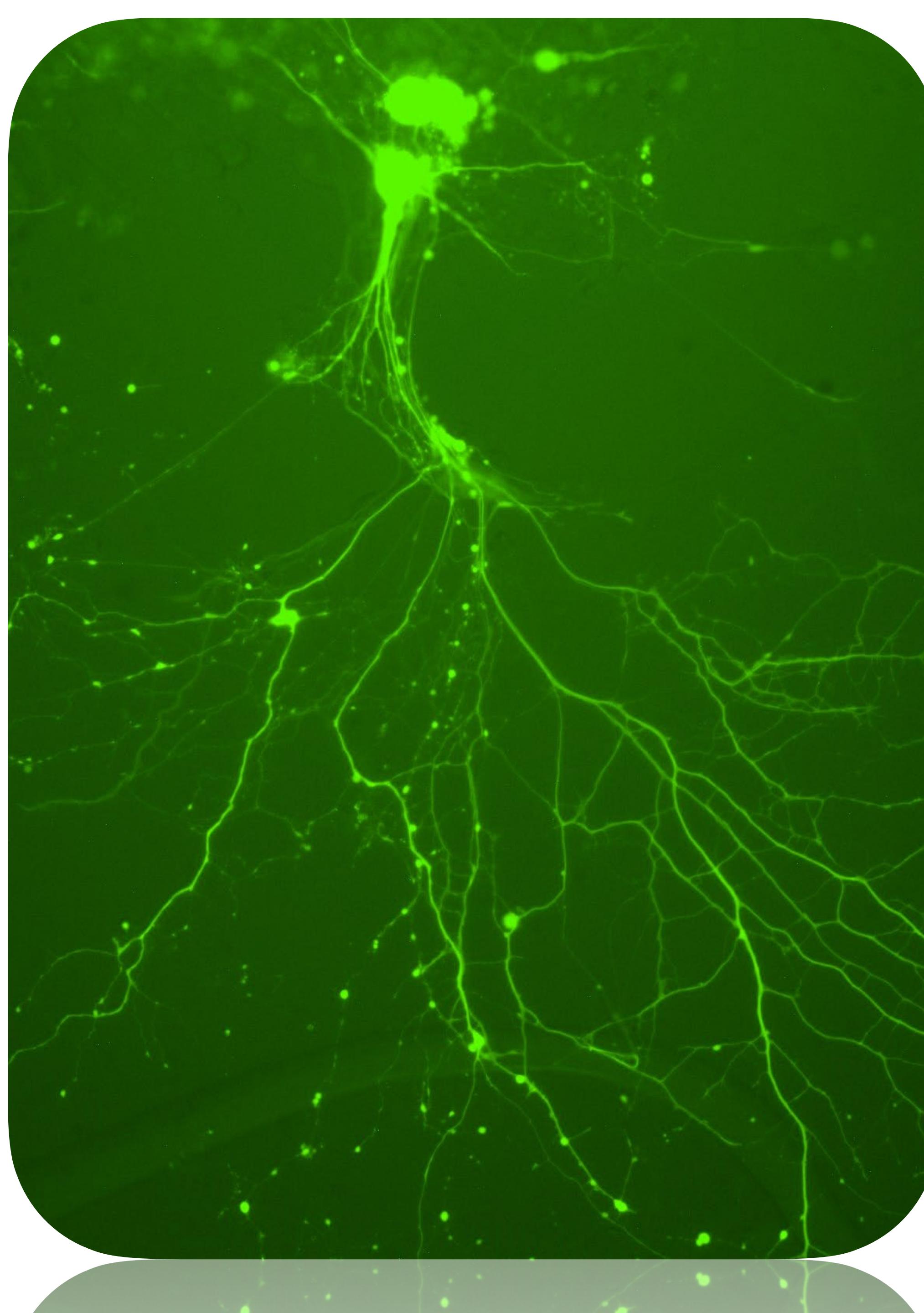
Direction Control

(f)

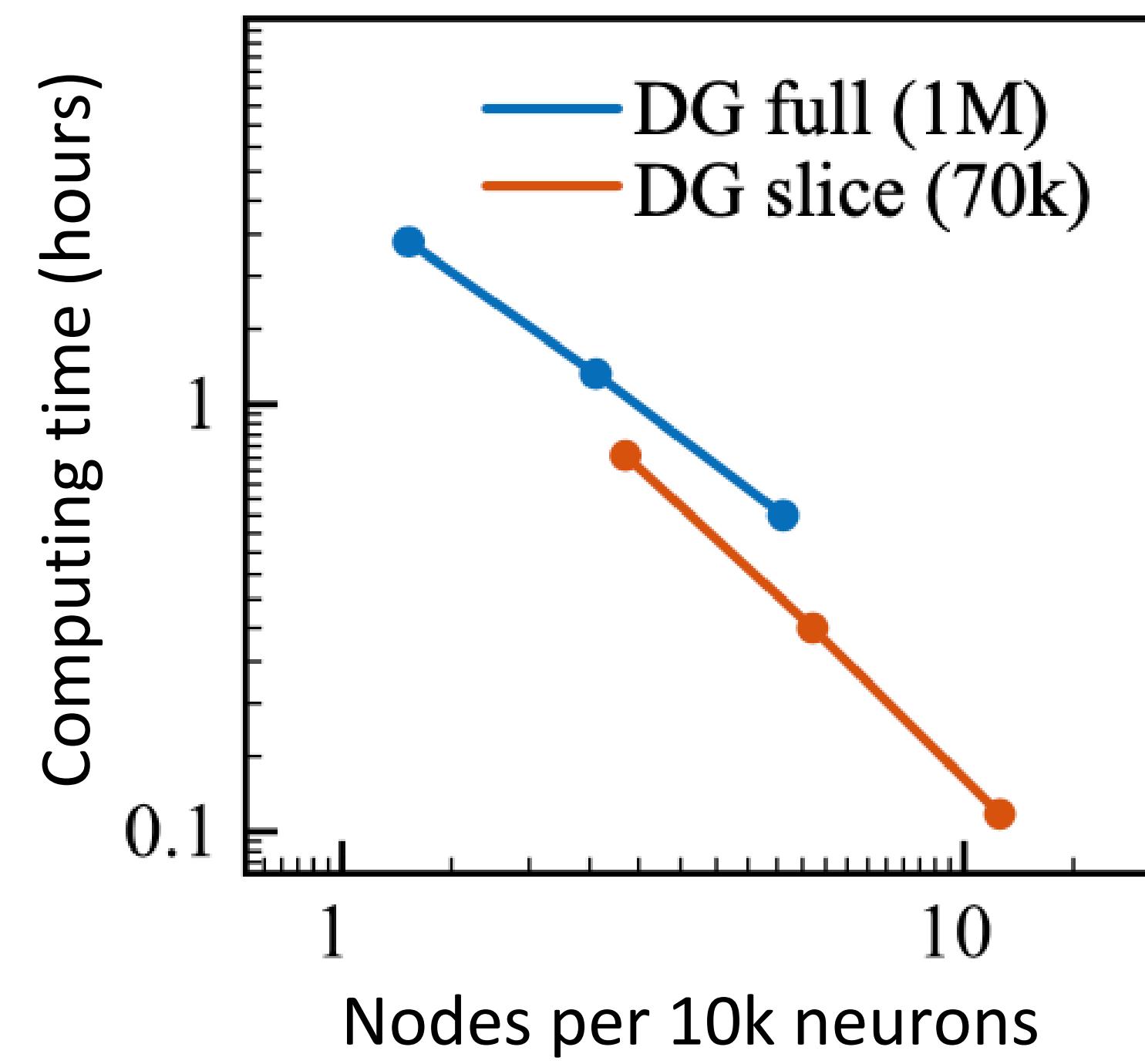
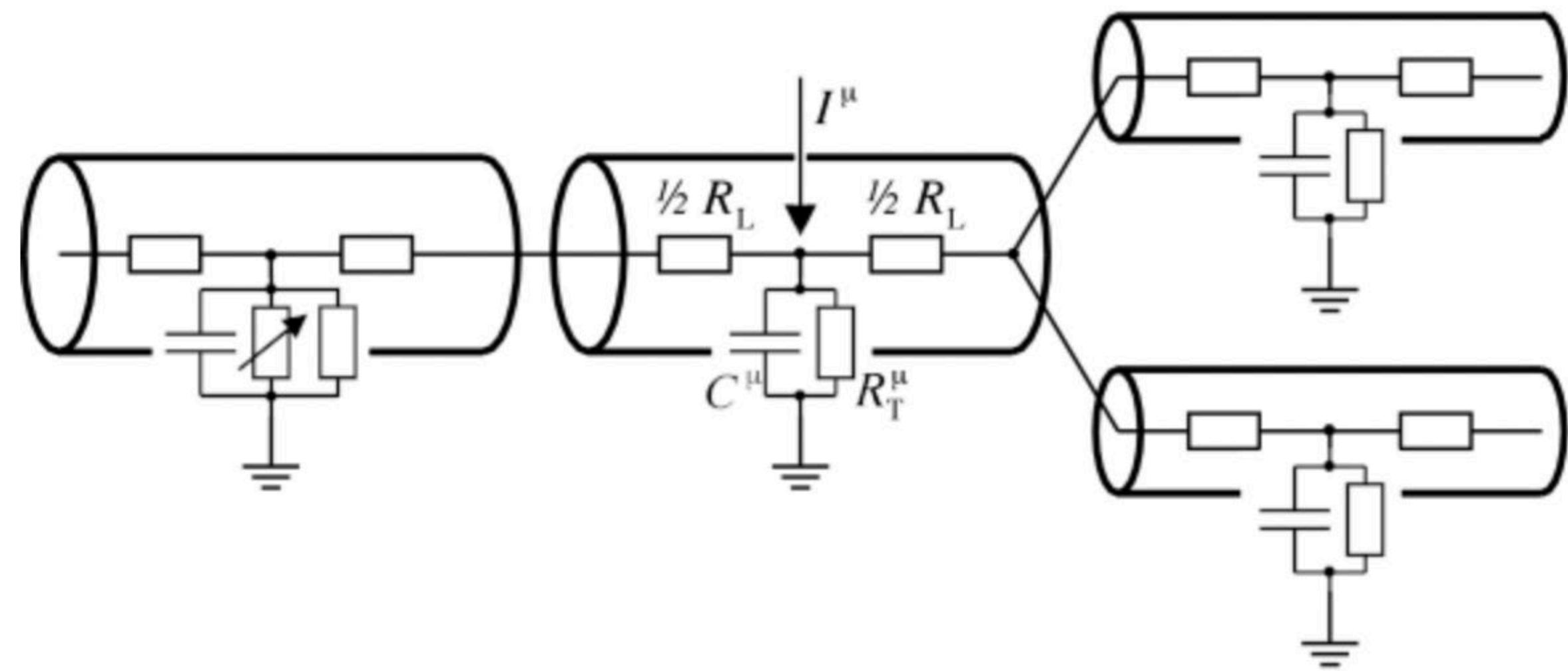
Ivan Raikov

Ivan Soltesz, Stanford



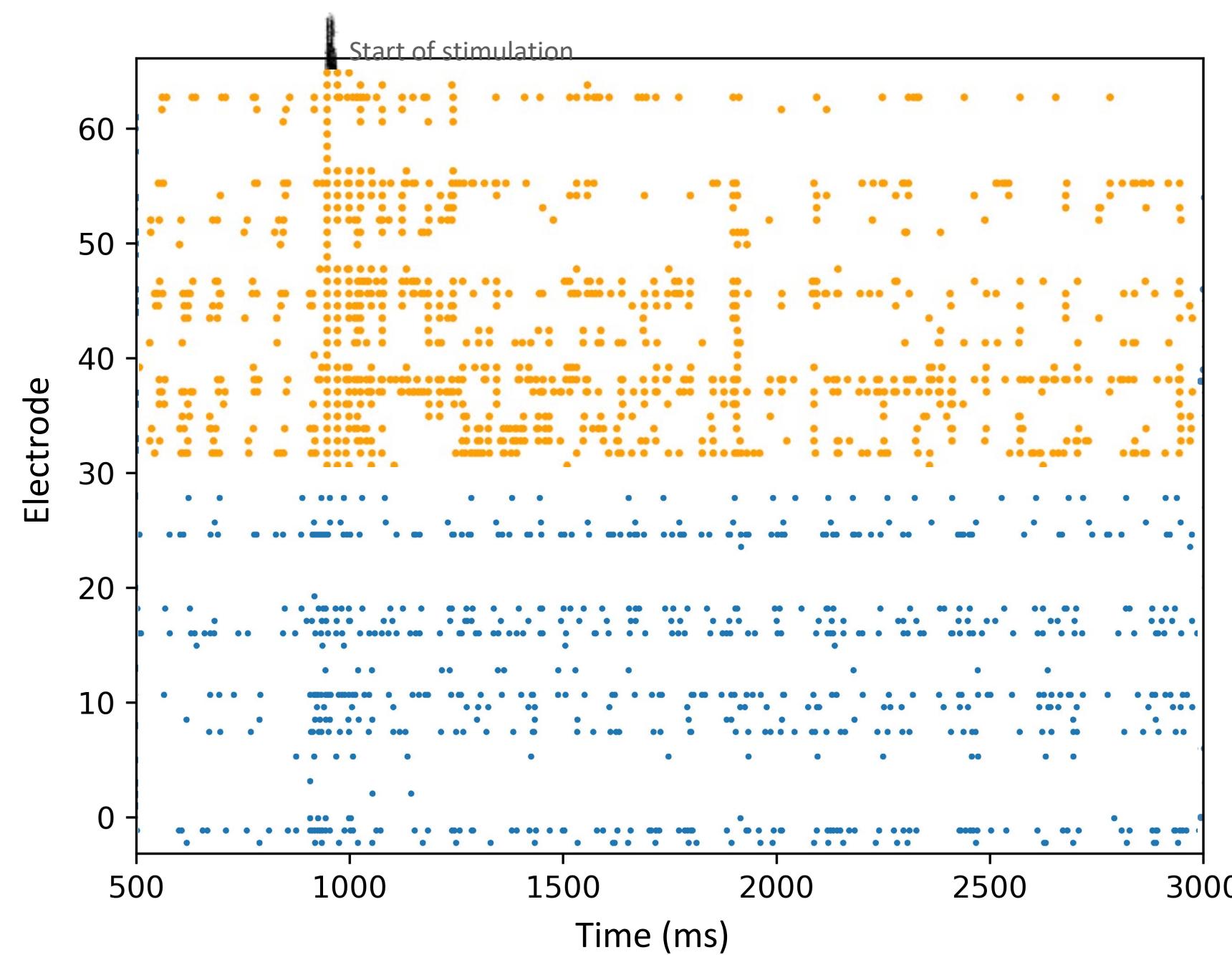
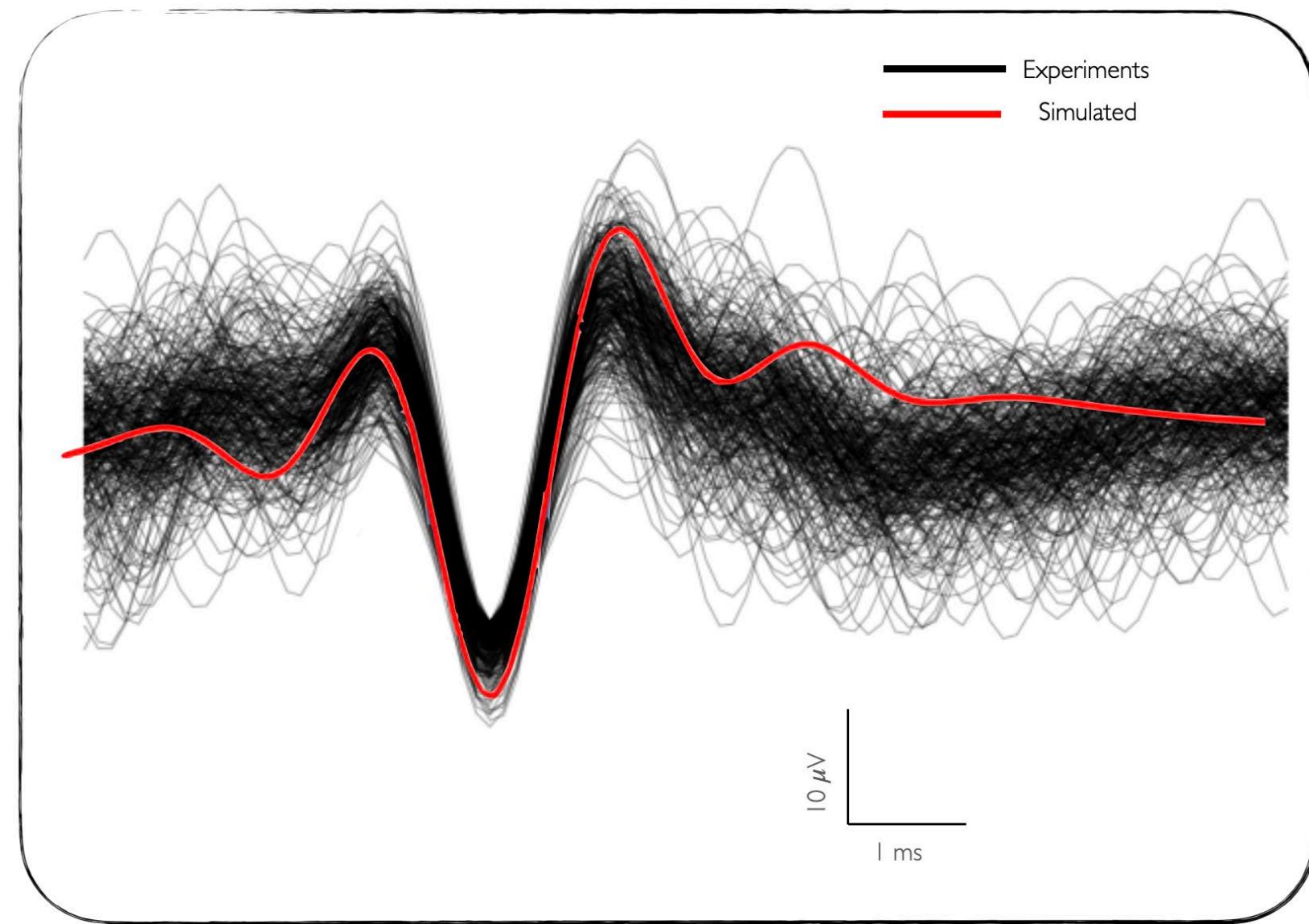


## Neuron Morphology :

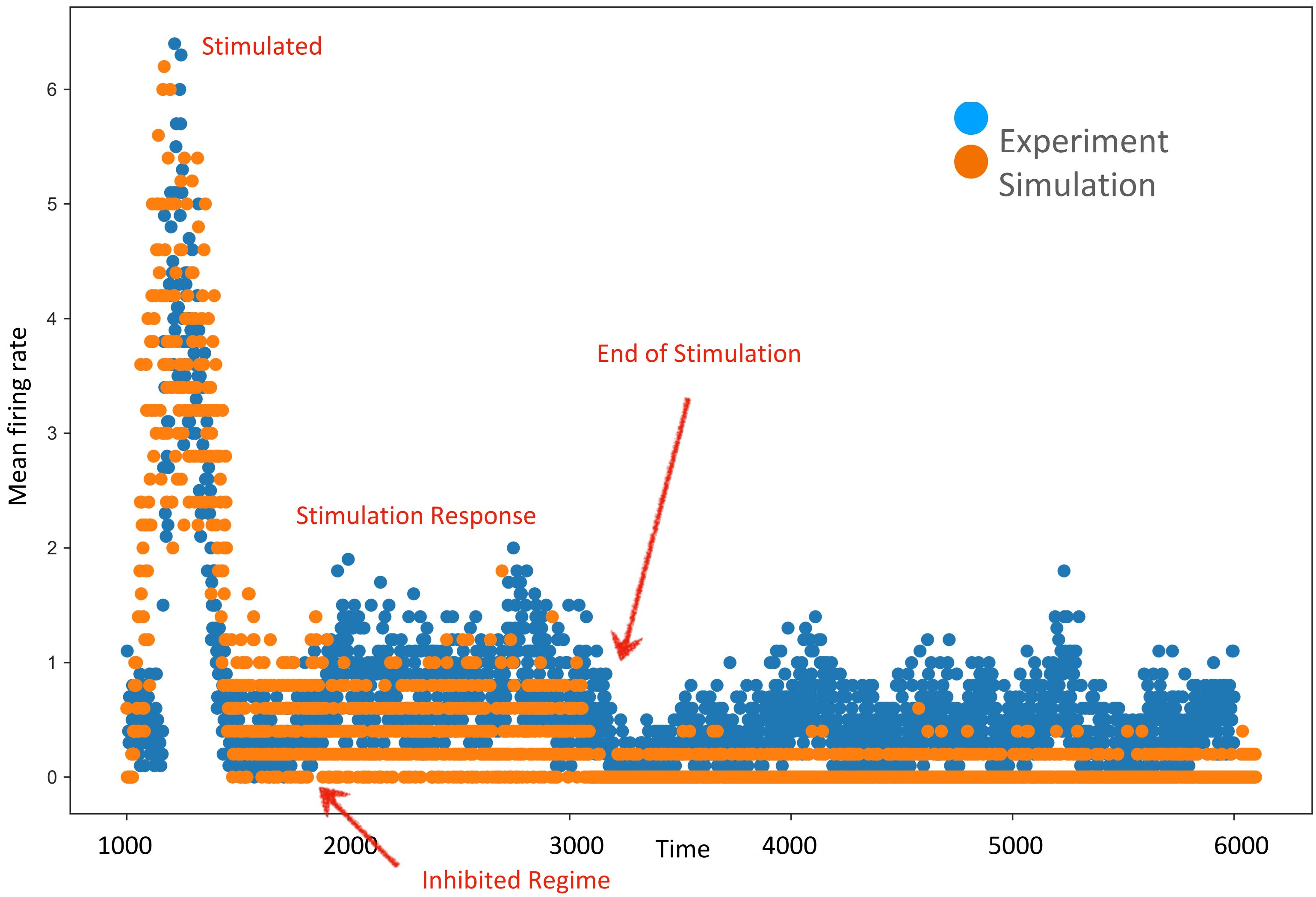


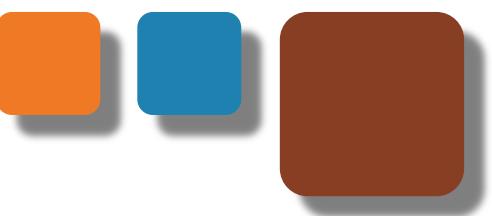
1 million neurons  
~20 billion synapses!

# Spike Profile

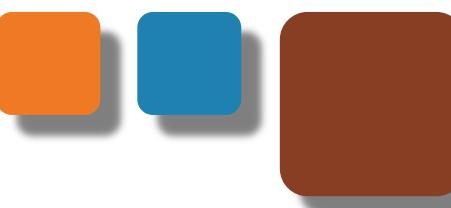


# Mean firing rate

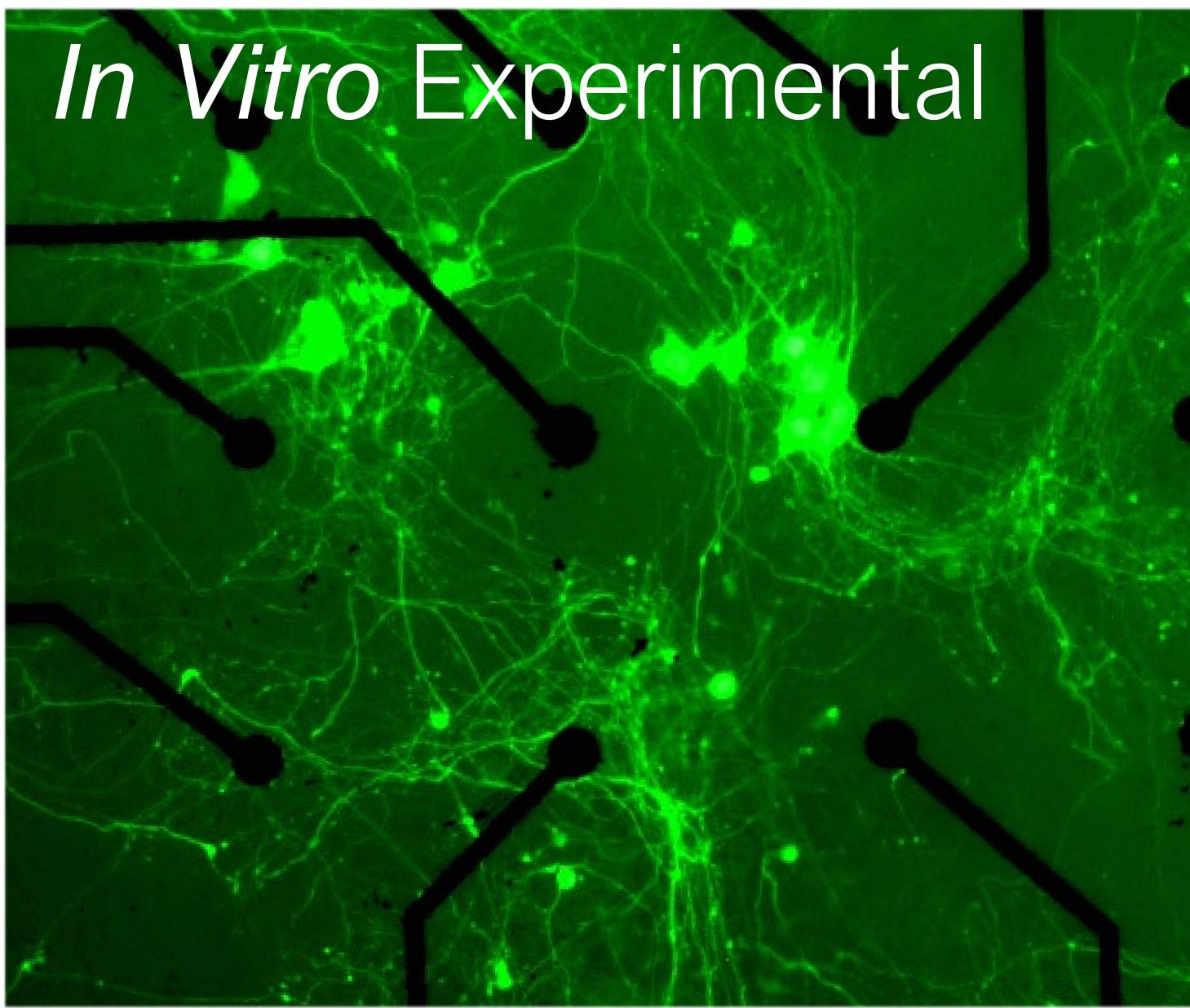




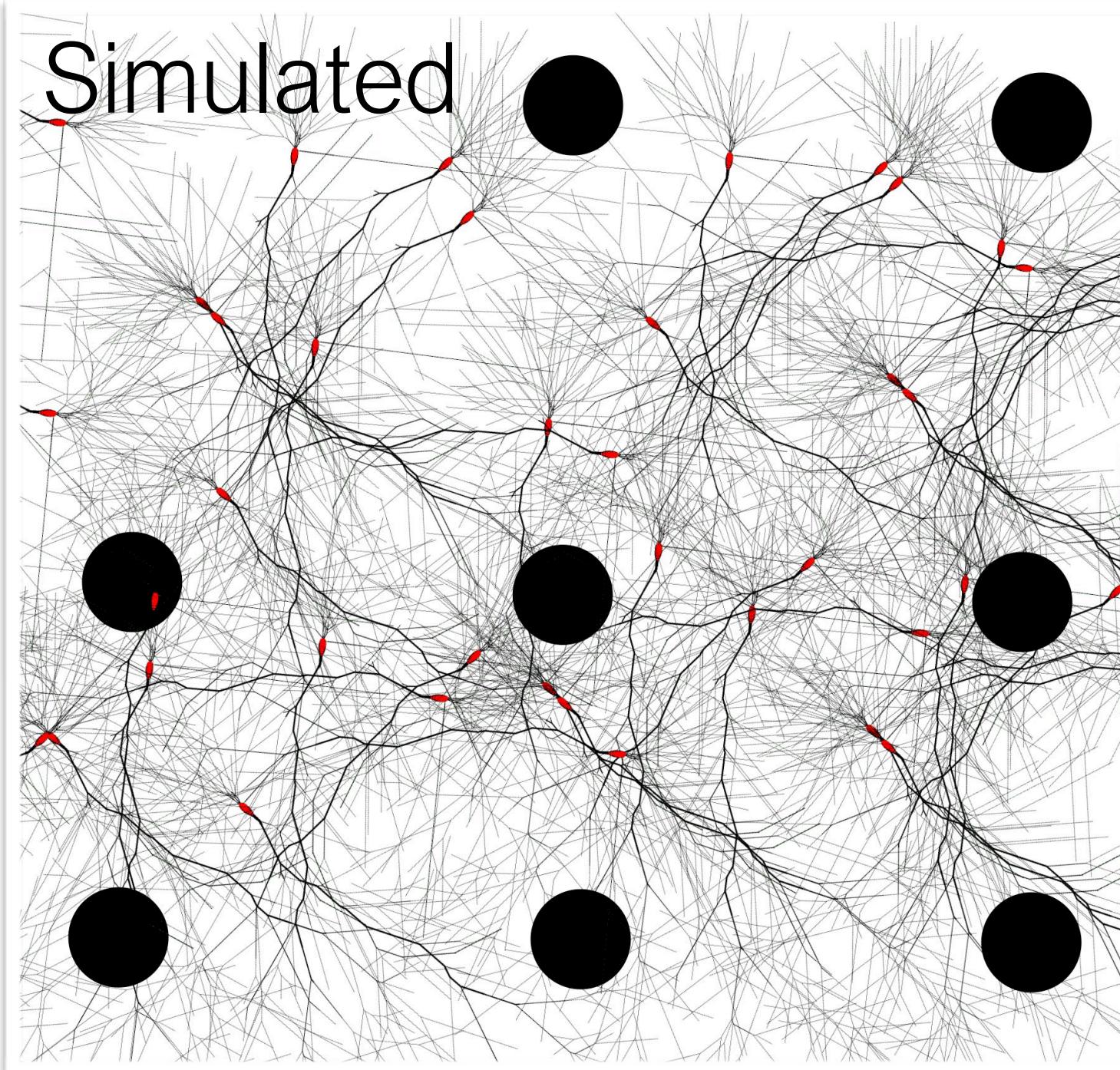
# MiV Software Stack in Frontera



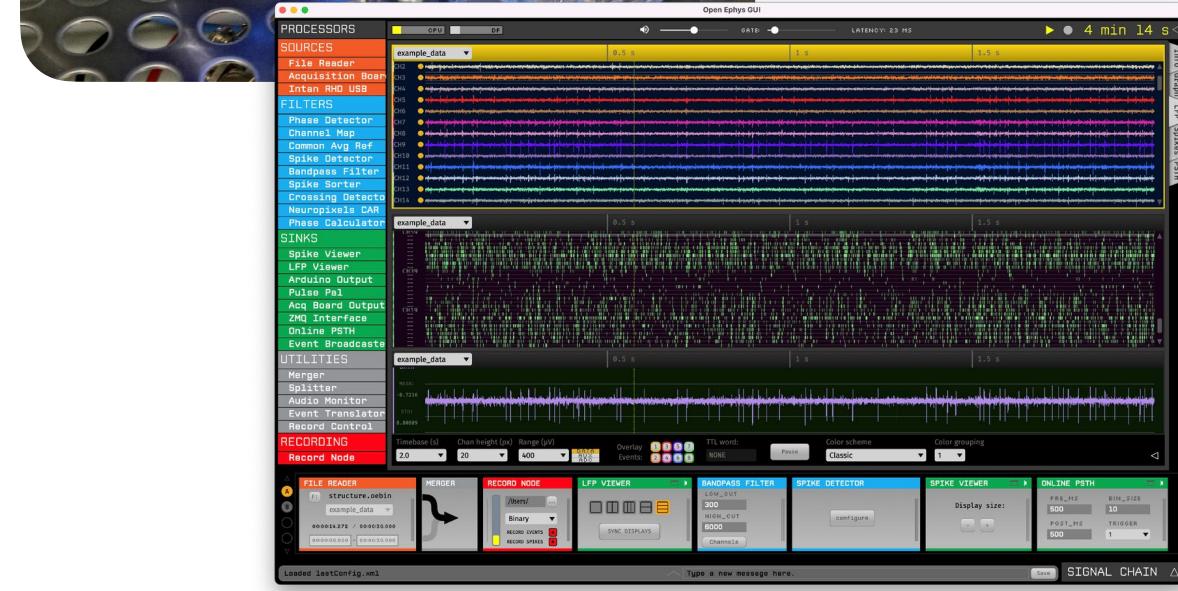
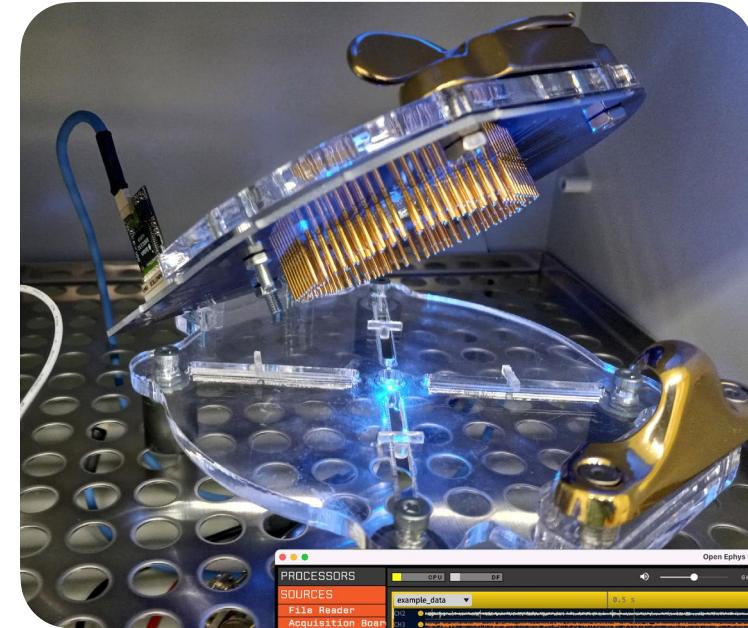
# In Vitro Experimental



# Simulated



## Electrophysiology

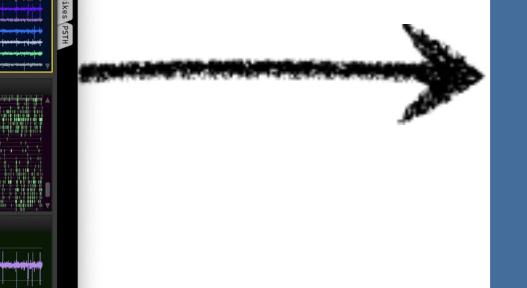


## Frontera



MiV-Simulator

Other Spike  
Data Library



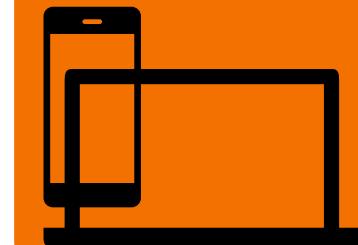
MiV Open Source

## Data Storage

- Data Archives
- Cloud

## HPC Tools

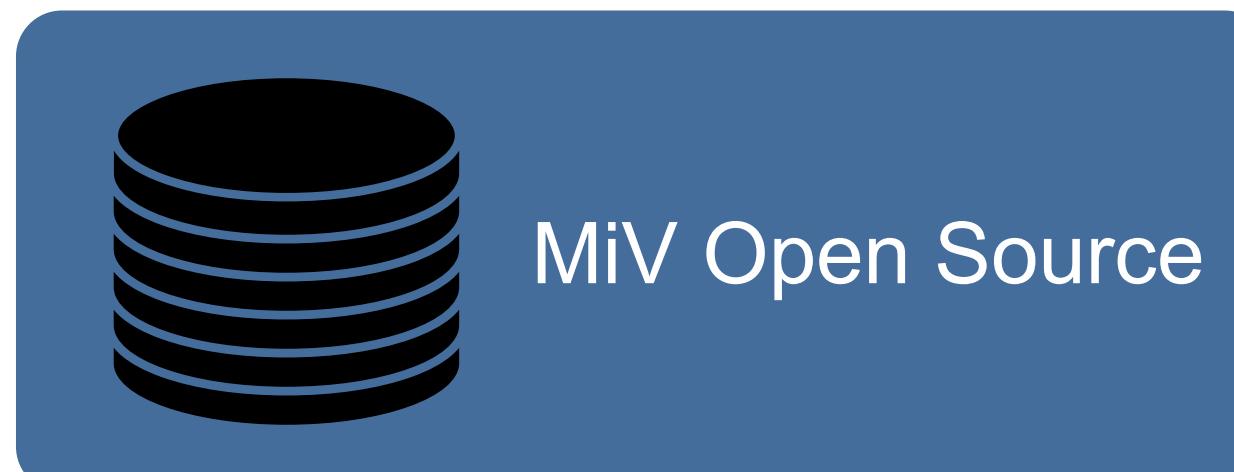
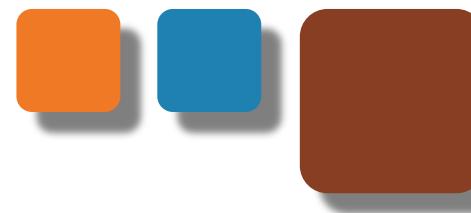
- Analysis
- Statistics
- Optimization
- Parameter search



MiV-Interface  
(Frontend)

- Interactive Analysis

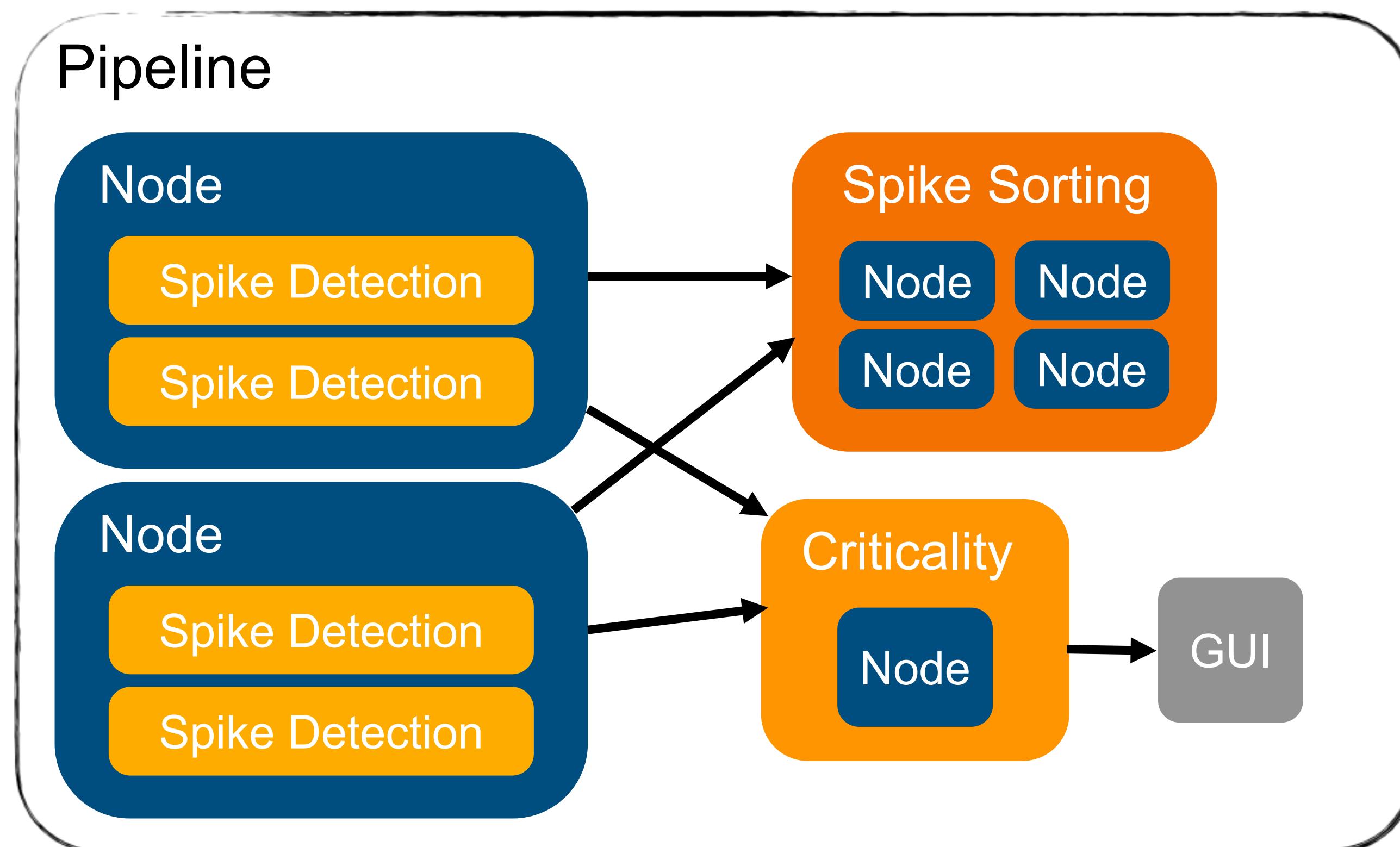
# HPC Data Post Processing



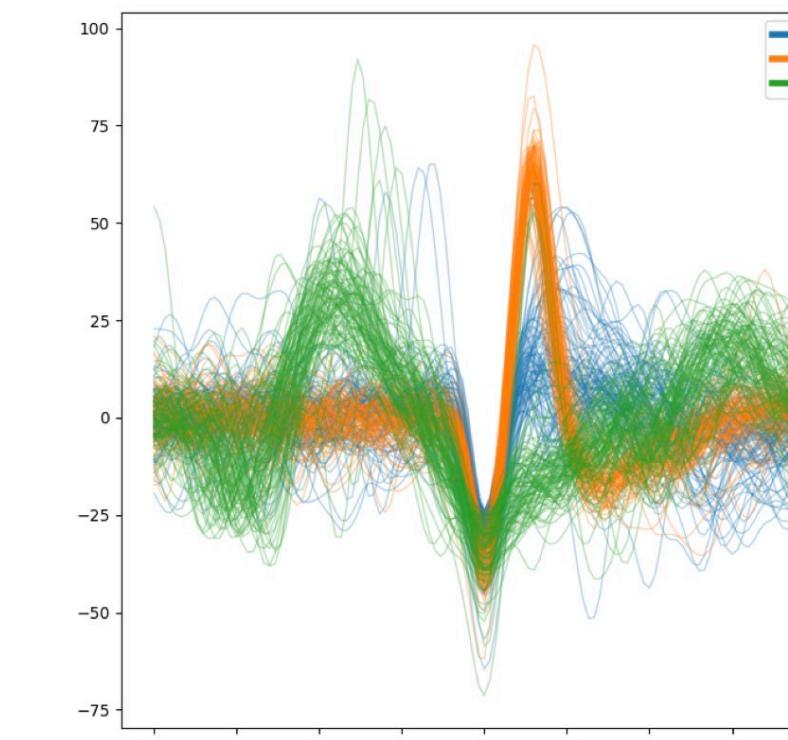
MiV Open Source

Multi-channel  
Time-series data

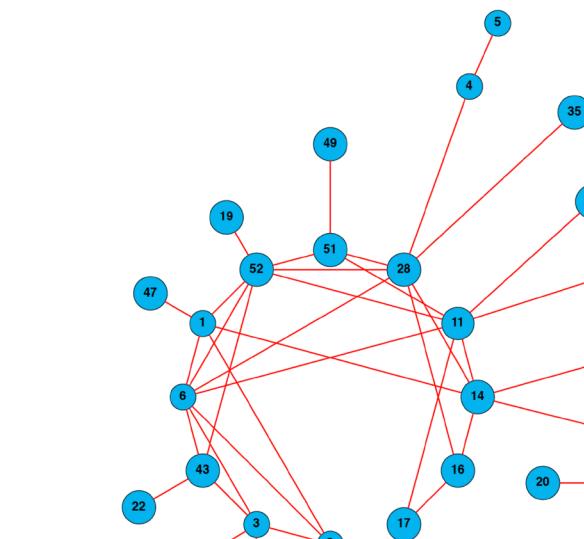
- Partition data block
- Manage I/O per nodes+cores
- Caching
- High Throughput Execution
- Dependency



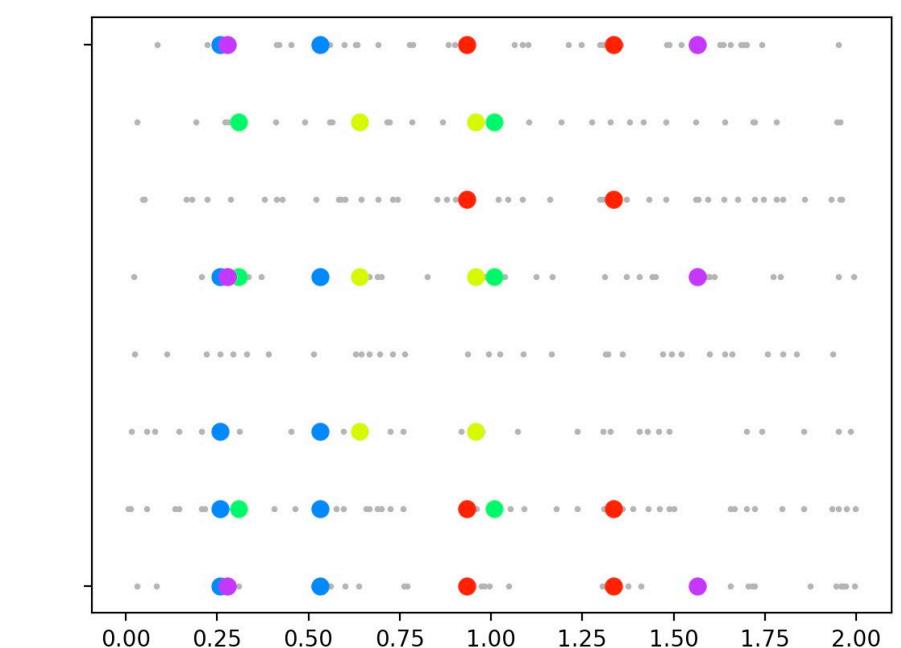
Spike Sorting (spyking-circus)<sup>[1]</sup>



Information Causality<sup>[2]</sup>



Cell Assembly<sup>[3]</sup>

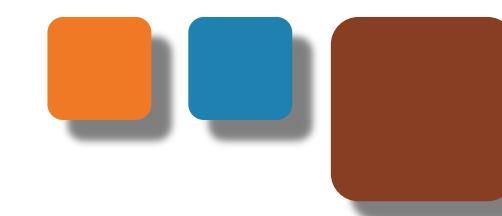


[1] Iger P., 2018

[2] Wollstadt P., 2018

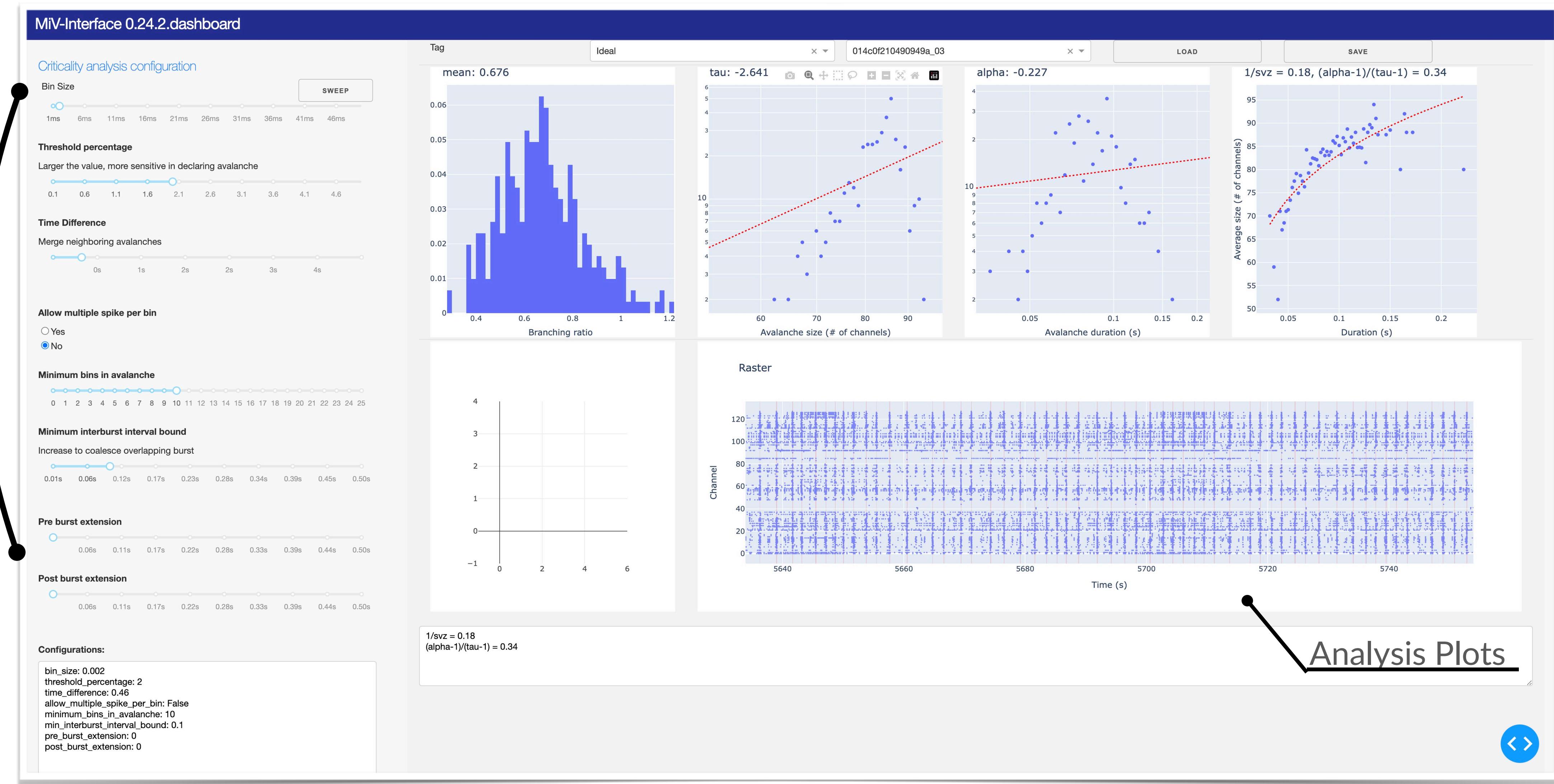
[3] Russo E., 2017

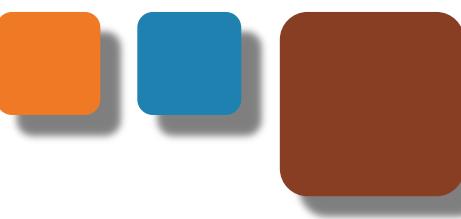
# HPC Interactive Data Processing



- Pilot launch — Interactive hyperparameter search

Hyperparameters from Pipeline



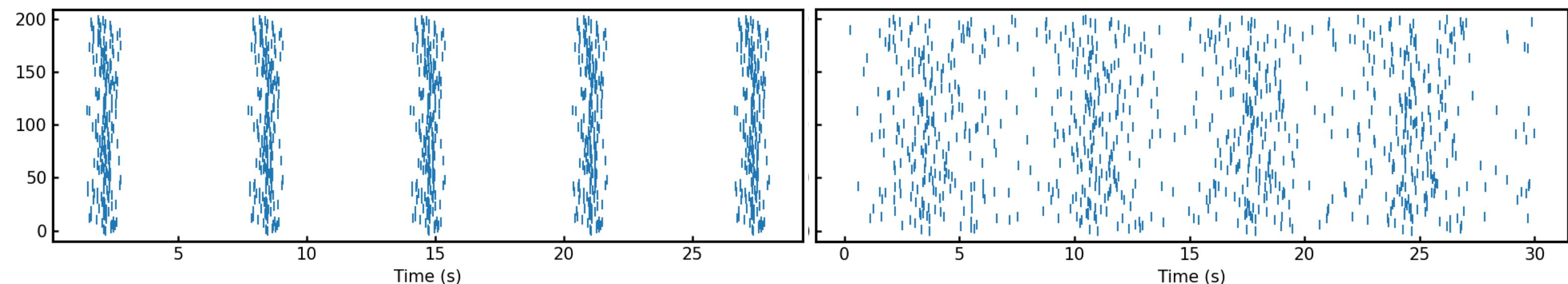


# Optimizing Cellular Substrate



Information Theory

Simulation



Sethna, Dahmen, Myers, Nature, 2001

Beggs, Plenz, Journal of Neuroscience, 2003

Chialvo, Nature Physics, 2010

<https://mindinvitro.illinois.edu>



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**Frithjof  
Gressmann  
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**Lawrence  
Rauchwerger  
UIUC**



**Ivan Raikov  
Stanford**



**Ivan Soltesz  
Stanford**

**TACC**  
TEXAS ADVANCED COMPUTING CENTER

